

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D@25^{\circ}C$
650V	95mΩ@18V	38A

Feature

- Wide bandgap SiC MOSFET technology
- Low On-Resistance with high blocking voltage
- Low capacitances with High-Speed switching
- Low reverse recovery(Qrr)

Application

- Switch mode power supplies
- Renewable energy
- On board charger
- High voltage DC/DC converters

Package

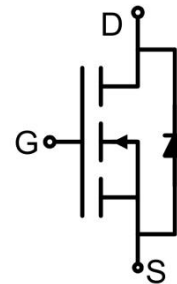


TO-247-3L

Marking



Circuit diagram



Absolute maximum ratings ($T_C=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Condition	Value	Unit
Drain-Source Voltage	V_{DSmax}	$V_{GS}=0V, I_D=100\mu A$	650	V
Gate-Source Voltage	V_{GSmax}	$t_p \leq 500ns, \text{duty cycle} \leq 1\%$	-8/+20	V
Gate-Source Voltage	V_{GSOP}	Static	-4/+18	V
Continuous Drain Current	I_D	$V_{GS}=18V$	38	A
	I_D	$V_{GS}=18V, T_C=100^{\circ}C$	27	A
Pulsed Drain Current	$I_{D,pulse}$	Pulse with t_p limited by T_{jmax}	48	A
Single Pulse Avalanche Energy	E_{AS}	$V_{DS}=650V, V_{DD}=50V, V_{GS}=10V, L=10mH$	198	mJ
Power Dissipation	P_D	$T_J=175^{\circ}C$	150	W
Thermal Resistance (Typ)	$R_{\theta JC}$	Junction-to-Case	1	$^{\circ}C/W$
Junction Temperature	T_J		-55~ +175	$^{\circ}C$
Storage Temperature	T_{STG}		-55~ +175	$^{\circ}C$

Electrical characteristics (T_J=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =100μA	650			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =650V, V _{GS} =0V			50	μA
Gate-Source leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =18V			250	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =4mA		3		V
		V _{DS} =V _{GS} , I _D =4mA, T _J =175°C		2		
Drain-source on-resistance	R _{DS(on)}	V _{GS} =18V, I _D =10A		95	125	mΩ
		V _{GS} =18V, I _D =10A, T _J =175°C		135		
Transconductance	g _{fs}	V _{GS} =18V, I _D =10A		7		S
		V _{GS} =18V, I _D =10A, T _J =175°C		6		
Dynamic characteristics						
Input Capacitance	C _{iss}	V _{DS} =600V, V _{GS} =0V V _{AC} =25mV, f =1MHz		803		pF
Output Capacitance	C _{oss}			77		
Reverse Transfer Capacitance	C _{rss}			5		
Total Gate Charge	Q _g	V _{DS} =400V, V _{GS} =-4V/18V I _D =10A		34		nC
Gate-Source Charge	Q _{gs}			7.8		
Gate-Drain Charge	Q _{gd}			9		
Internal Gate Resistance	R _{G(int)}	V _{AC} =25mV, f =1MHz		3.8		Ω
Turn-on delay time	t _{d(on)}	V _{DS} =400V, V _{GS} =-4V/+18V I _D =10A, R _{G(ext)} =5Ω, L =294μH		5		nS
Turn-on rise time	t _r			18		
Turn-off delay time	t _{d(off)}			17		
Turn-off fall time	t _f			10		
Turn-on Switching Energy	E _{on}			43		μJ
Turn-off Switching Energy	E _{off}			17		
Total switching energy	E _{tot}			60		
Source-Drain Diode characteristics						
Diode Forward Current	I _S	V _{GS} =-5V, T _C =25°C		37		A
Diode Forward voltage	V _{SD}	V _{GS} =-5V, I _{SD} =5A		1.4	4	V
		V _{GS} =-5V, I _{SD} =5A, T _J =175°C		1.9	4.5	
Reverse Recovery Time	T _{rr}	V _{GS} =-4V, I _{SD} =10A, V _R =400V dif/dt =1000 A/μs		29		nS
Reverse Recovery Charge	Q _{rr}			63		nC
Peak Reverse Recovery Current	I _{rrm}				5	

Typical Characteristics

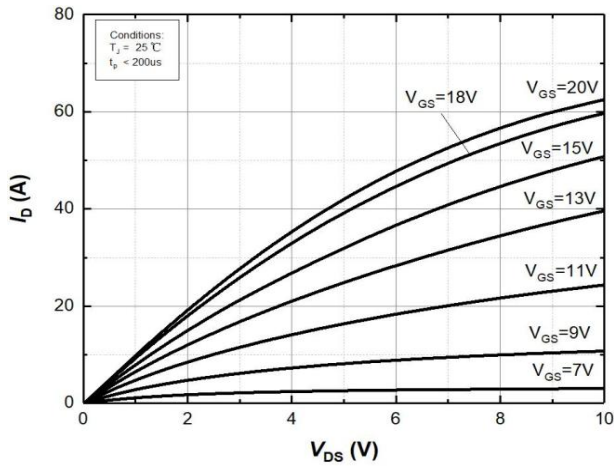


Figure 1. Output characteristics at $T_j=25^{\circ}\text{C}$

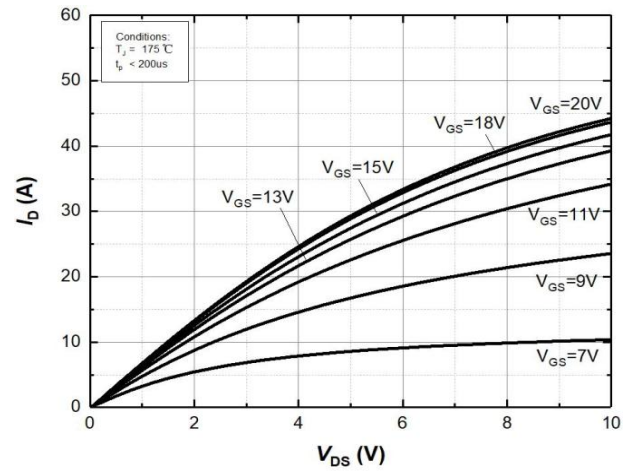


Figure 2. Output characteristics at $T_j=175^{\circ}\text{C}$

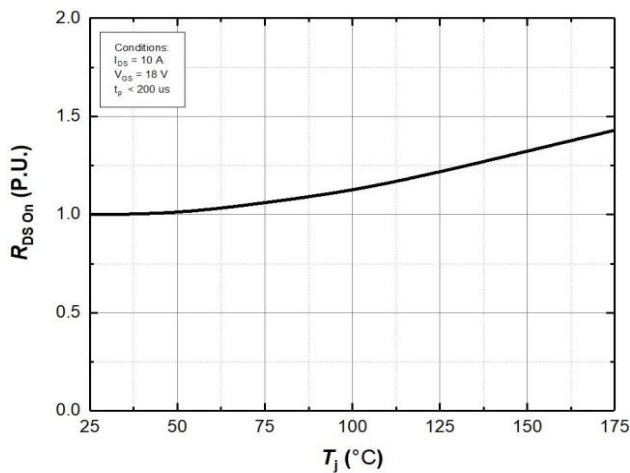


Figure 3. Normalized On-Resistance vs. Temperature

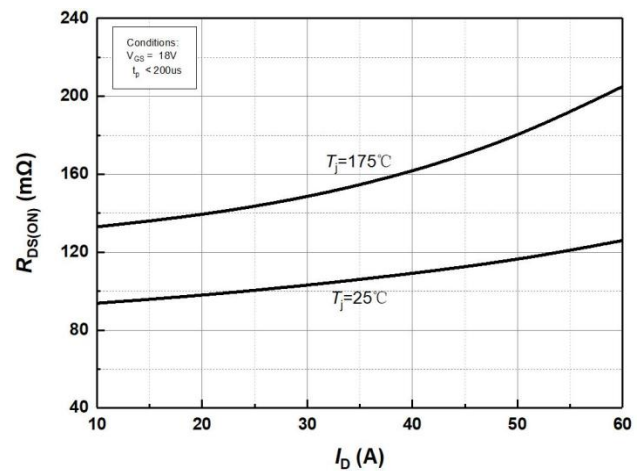


Figure 4. On-Resistance vs. Drain current for Various Temperature

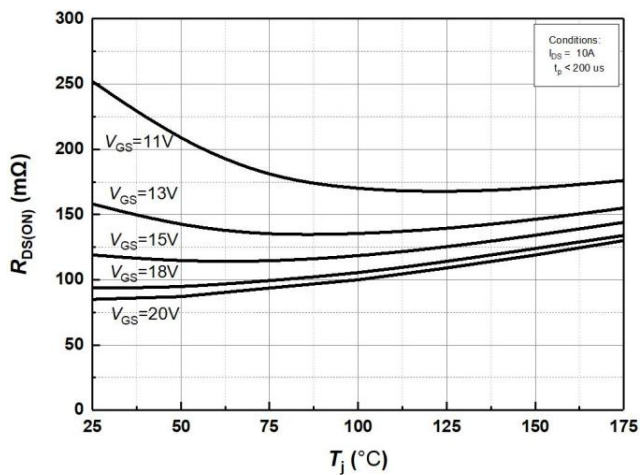


Figure 5. On-Resistance vs. Temperature for Various Gate Voltage

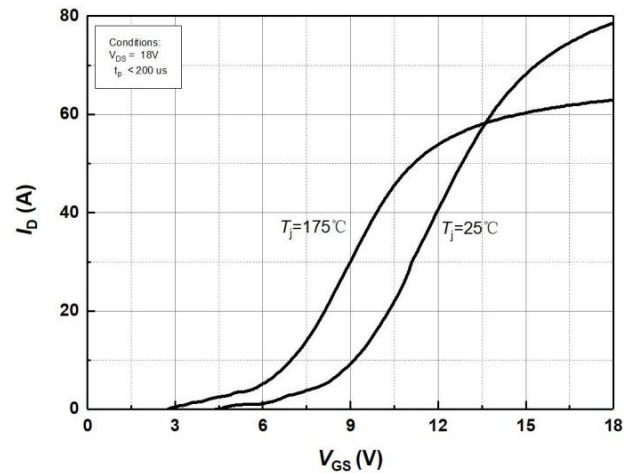


Figure 6. Transfer Characteristics for Various Junction Temperatures

Typical Characteristics

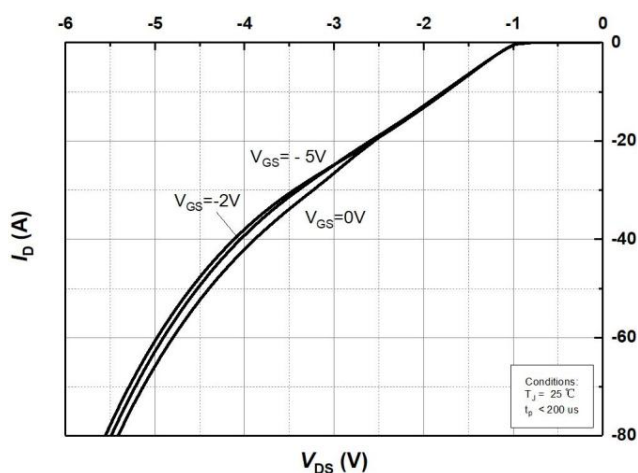


Figure 7. Body Diode Characteristics at Tj=25°C

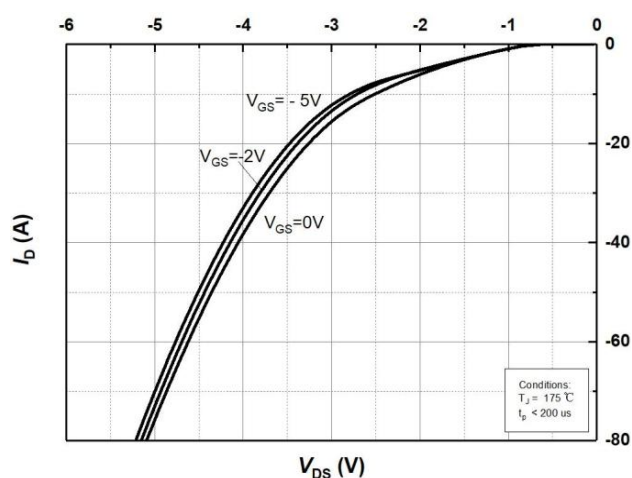


Figure 8. Body Diode Characteristics at Tj=175°C

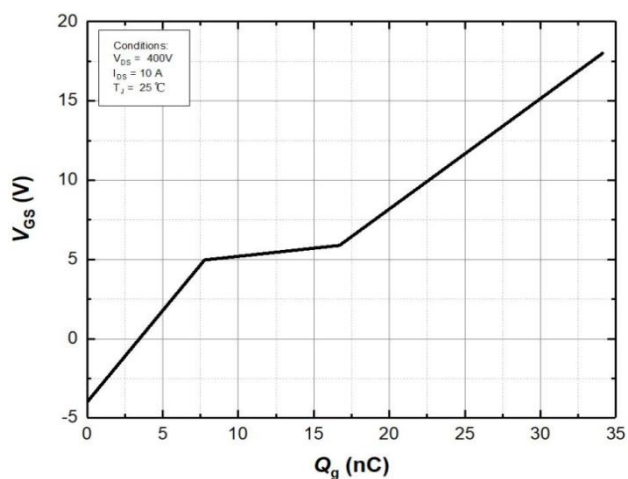
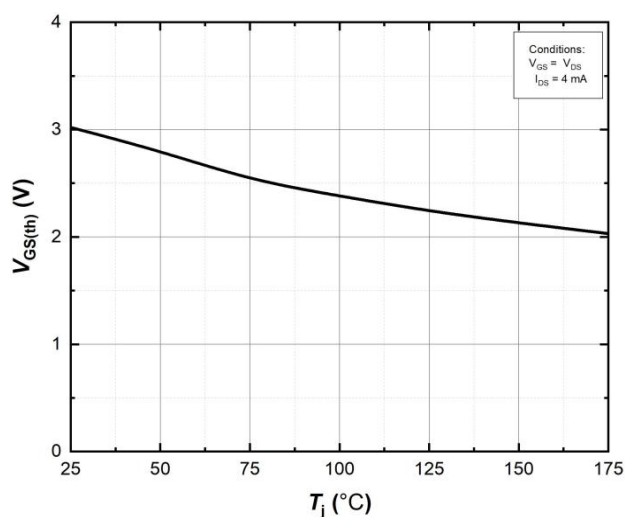


Figure 10 Gate Charge Characteristics

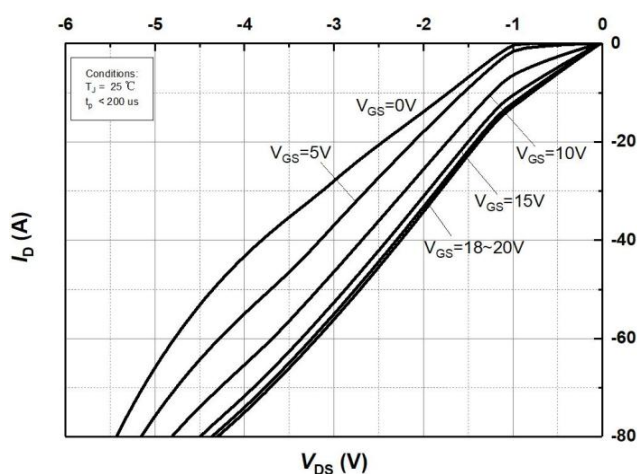


Figure 11. 3rd Quadrant Characteristic at Tj=25°C

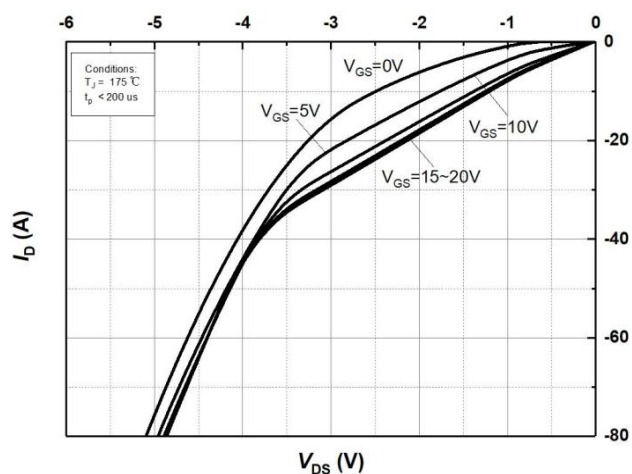


Figure 12. 3rd Quadrant Characteristic at Tj=175°C

Typical Characteristics

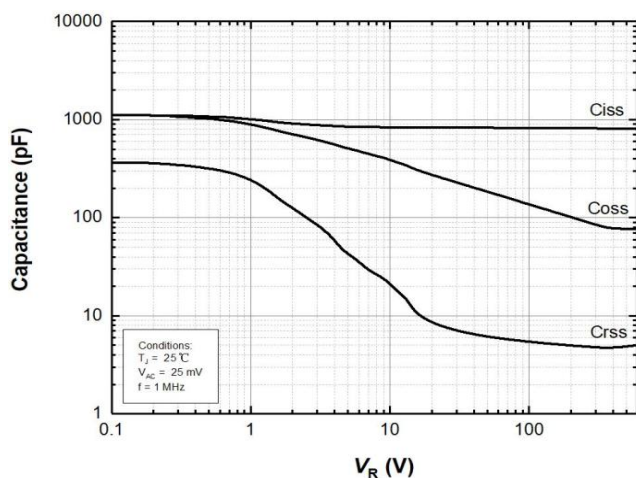


Figure 13. Capacitances vs. Drain-Source Voltage (0 – 600V)

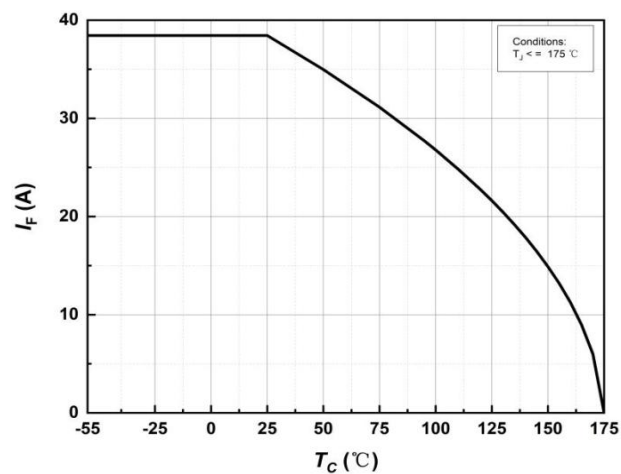


Figure 14. Continuous Drain Current Derating vs Case Temperature

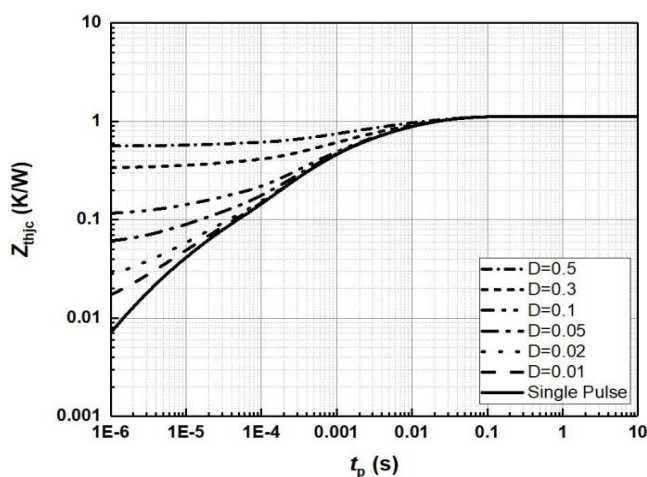


Figure 15. Transient Thermal Impedance (Junction – Case)

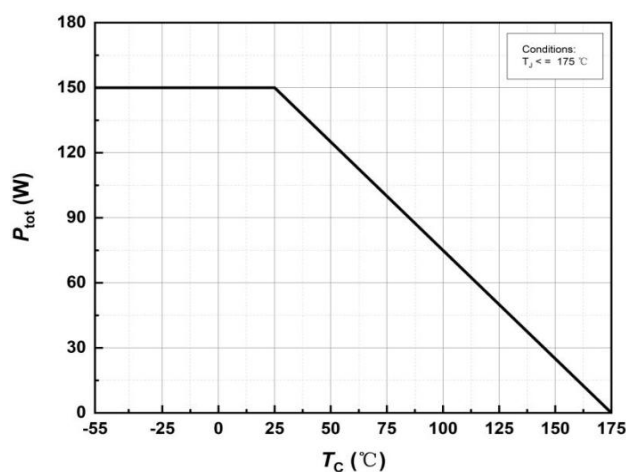


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

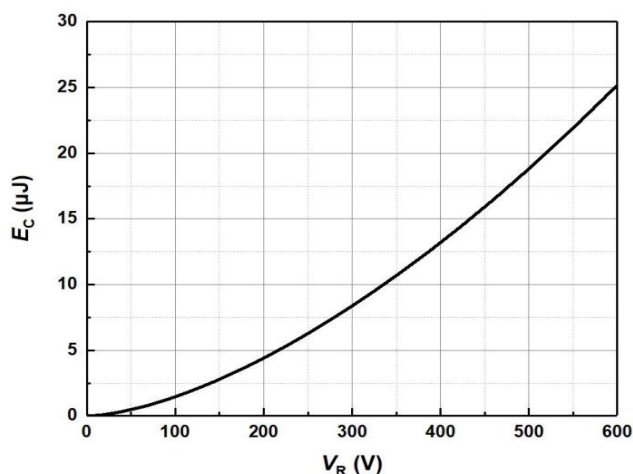


Figure 17. Output Capacitor Stored Energy

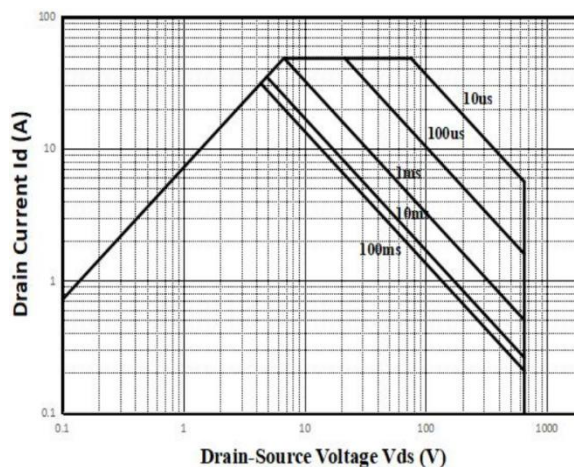


Figure 18. Safe Operating Area

Typical Characteristics

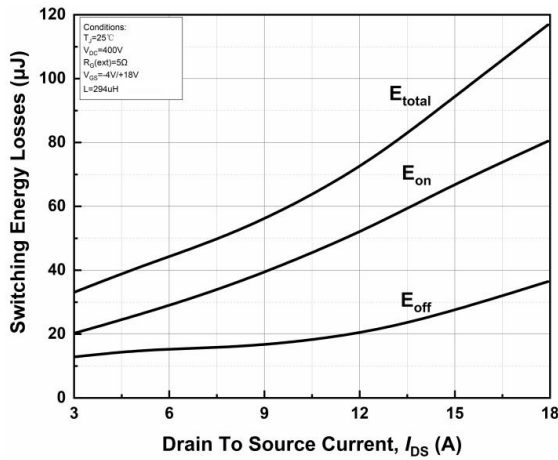


Figure 19. Clamped Inductive Switching Energy vs. Drain Current ($V_{DS} = 400\text{V}$)

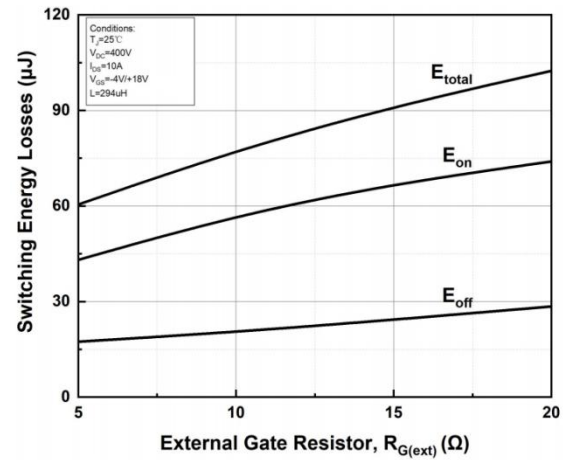


Figure 20. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

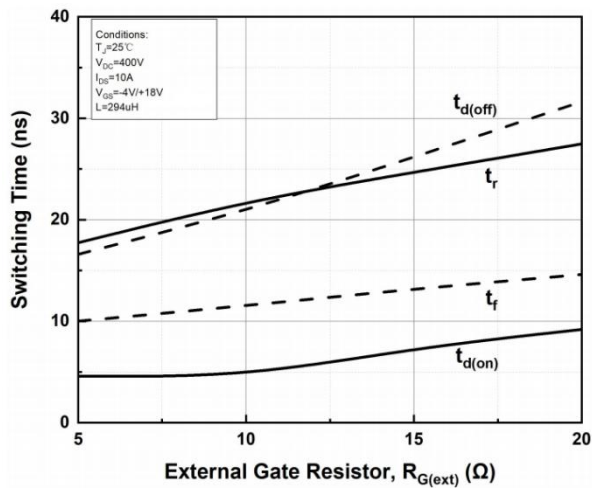
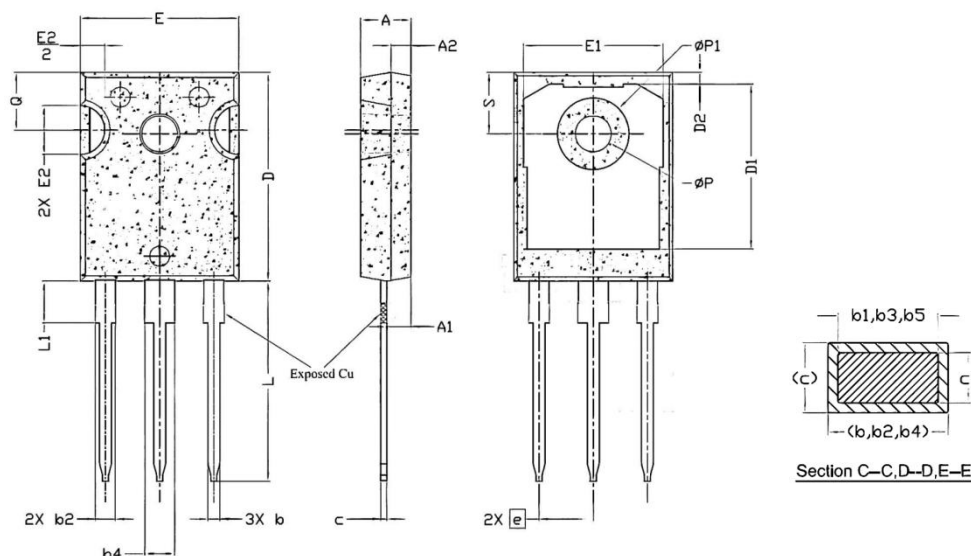


Figure 21. Switching Times vs. $R_{G(ext)}$

TO-247-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.830	5.210	0.190	0.205
A1	2.290	2.550	0.090	0.100
A2	1.500	2.490	0.059	0.098
b	1.120	1.330	0.044	0.052
b1	1.120	1.280	0.044	0.050
b2	1.910	2.390	0.075	0.094
b3	1.910	2.340	0.075	0.092
b4	2.870	3.220	0.113	0.127
b5	2.870	3.180	0.113	0.125
c	0.550	0.690	0.022	0.027
c1	0.550	0.650	0.022	0.026
D	20.800	21.100	0.819	0.831
D1	16.250	17.650	0.640	0.695
D2	0.510	1.350	0.020	0.053
E	15.750	16.130	0.620	0.635
E1	13.460	14.160	0.530	0.557
E2	4.320	5.490	0.170	0.216
e	5.440 BSC		0.214 BSC	
L	19.810	20.320	0.780	0.800
L1	4.100	4.400	0.161	0.173
ØP	3.560	3.650	0.140	0.144
ØP1	7.190 REF.		0.283 REF.	
Q	5.390	6.200	0.212	0.244
S	6.040	6.300	0.238	0.248