

Product Summary

| $V_{(BR)DSS}$ | $R_{DS(on)MAX}$ | I_D |
|---------------|-----------------|-------|
| 60V | 6.9mΩ@10V | 75A |
| | 9.5mΩ@4.5V | |

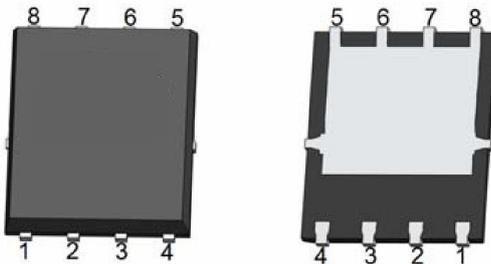
Feature

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Suffix "-Q1" for AEC-Q101

Application

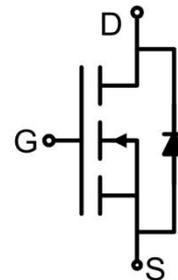
- DC-DC converter
- Engine management systems
- Body control electronics

Package

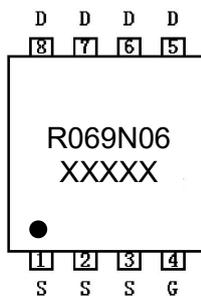


PDFN5*6-8L

Circuit diagram



Marking



Absolute maximum ratings (T_J=25°C unless otherwise noted)

| Parameter | Symbol | Value | Unit |
|---|------------------------|------------|------|
| Drain-Source Voltage | V _{DS} | 60 | V |
| Gate-Source Voltage | V _{GS} | ±20 | V |
| Continuous Drain Current(T _C =25°C) ¹⁾³⁾ | I _D | 75 | A |
| Continuous Drain Current(T _C =100°C) ¹⁾³⁾ | I _D (100°C) | 53 | A |
| Pulsed Drain Current(T _C =25°C, tp=100us) | I _{DM} | 270 | A |
| Power Dissipation(T _C =25°C) ¹⁾³⁾ | P _D | 83 | W |
| Thermal Resistance,Junction-to-Ambient ²⁾ | R _{θJA} | 55 | °C/W |
| Thermal Resistance,Junction-to-Case | R _{θJC} | 1.8 | °C/W |
| Single pulse avalanche energy (V _G =10V, R _G =25Ω, L=0.5mH, I _{AS} =25A) | E _{AS} | 156.25 | mJ |
| Junction Temperature | T _J | 175 | °C |
| Storage Temperature Range | T _{STG} | -55 ~ +175 | °C |

Electrical characteristics (T_J=25 °C, unless otherwise noted)

| Parameter | Symbol | Test Condition | Min. | Typ. | Max. | Unit |
|---|----------------------|---|------|------|------|------|
| Static Characteristics | | | | | | |
| Drain-source breakdown voltage | V _{(BR)DSS} | V _{GS} = 0V, I _D =250μA | 60 | | | V |
| Zero gate voltage drain current | I _{DSS} | V _{DS} =60V, V _{GS} = 0V | | | 1 | μA |
| | | V _{DS} =60V, V _{GS} = 0V, T _J =125°C | | | 100 | |
| Gate-body leakage current | I _{GSS} | V _{GS} =±20V, V _{DS} = 0V | | | ±100 | nA |
| Gate threshold voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =250μA | 1.3 | 1.8 | 2.3 | V |
| Drain-source on-resistance | R _{DS(on)} | V _{GS} =10V, I _D =20A | | 5.3 | 6.9 | mΩ |
| Drain-source on-resistance | | V _{GS} =4.5V, I _D =10A | | 7.0 | 9.5 | |
| Dynamic characteristics⁴⁾ | | | | | | |
| Input Capacitance | C _{iss} | V _{DS} =30V, V _{GS} =0V, f =1MHz | | 2060 | | pF |
| Output Capacitance | C _{oss} | | | 340 | | |
| Reverse Transfer Capacitance | C _{rss} | | | 16.8 | | |
| Gate resistance | R _G | f =1MHz | | 1.6 | | Ω |
| Total Gate Charge | Q _g | V _{DS} =30V, V _{GS} =10V, I _D =20A | | 27.2 | | nC |
| Gate-Source Charge | Q _{gs} | | | 5.6 | | |
| Gate-Drain Charge | Q _{gd} | | | 3.9 | | |
| Turn-on delay time | t _{d(on)} | V _{DD} =30V, V _{GS} =10V, I _D =20A, R _{GEN} =2.7Ω | | 11.6 | | nS |
| Turn-on rise time | t _r | | | 58 | | |
| Turn-off delay time | t _{d(off)} | | | 27.4 | | |
| Turn-off fall time | t _f | | | 5.8 | | |
| Source-Drain Diode characteristics | | | | | | |
| Diode Forward Current | I _S | | | | 75 | A |
| Diode Forward voltage | V _{SD} | V _{GS} =0V, I _S =20A | | | 1.2 | V |
| Reverse Recovery Time | t _{rr} | I _F =20A, di/dt =100A/μs | | 23.3 | | nS |
| Reverse Recovery Charge | Q _{rr} | | | | 17.3 | |

Notes:

- 1) The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2) The value of R_{θJA} measured the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1in² pad of 2oz. Copper, in a still air environment with T_A =25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 3) Thermal resistance from junction to soldering point (on the exposed drain pad).
- 4) Guaranteed by design, not subject to production testing.

Typical Characteristics

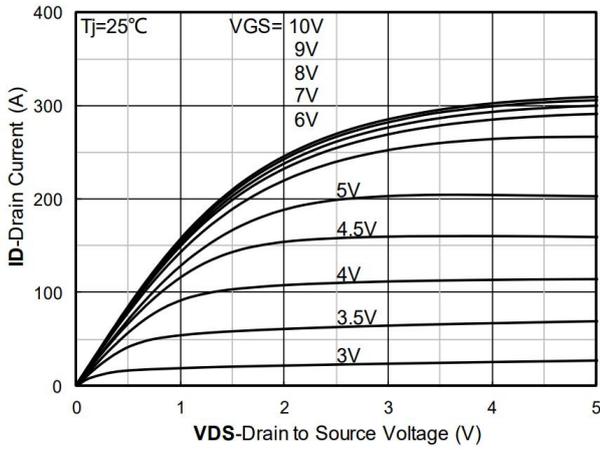


Figure 1. Output Characteristics

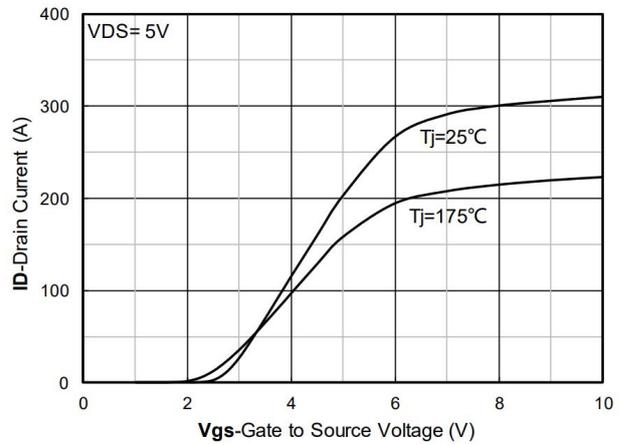


Figure 2. Transfer Characteristics

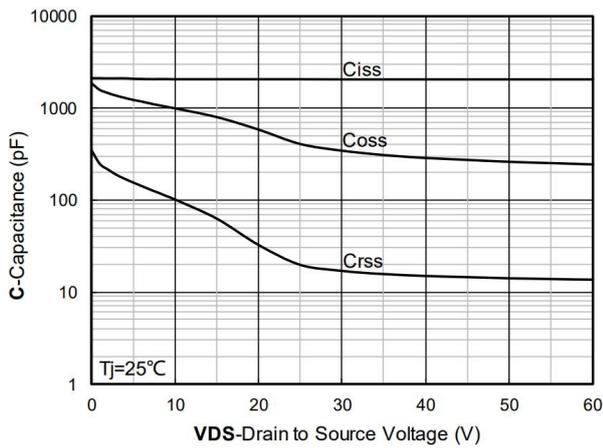


Figure 3. Capacitance Characteristics

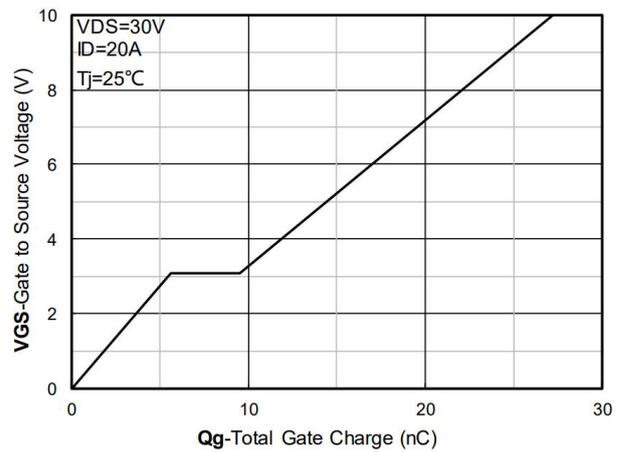


Figure 4. Gate Charge

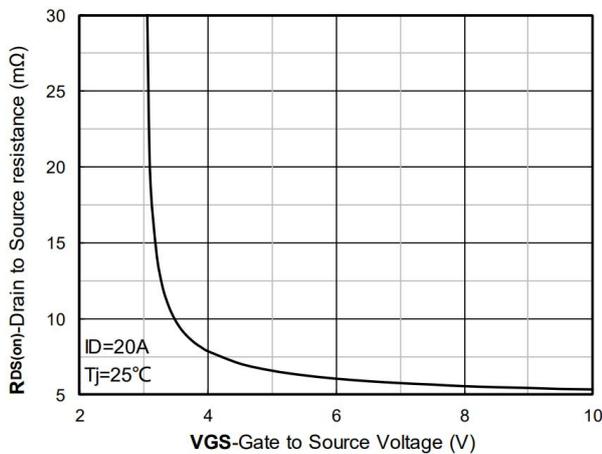


Figure 5. On-Resistance vs Gate to Source Voltage

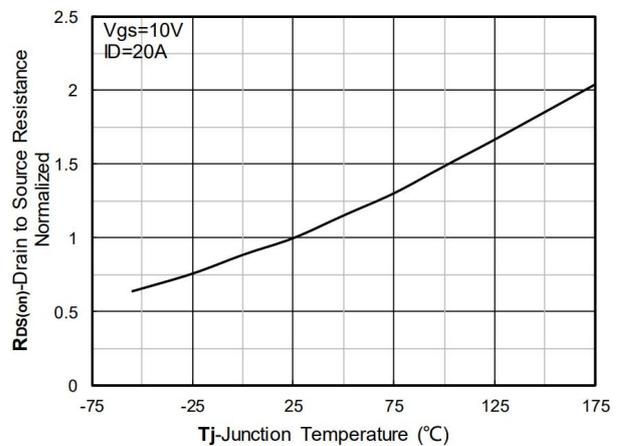


Figure 6. Normalized On-Resistance

Typical Characteristics

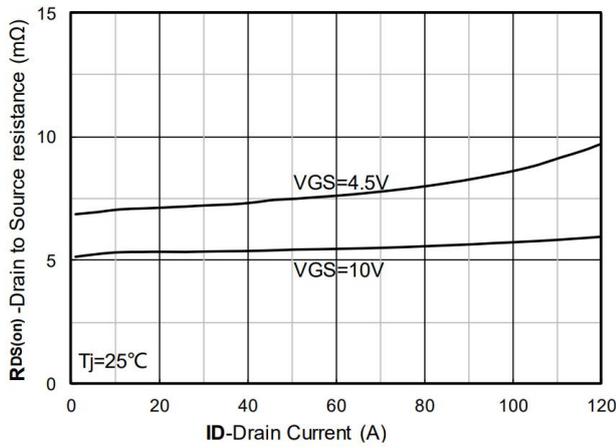


Figure 7. RDS(on) VS Drain Current

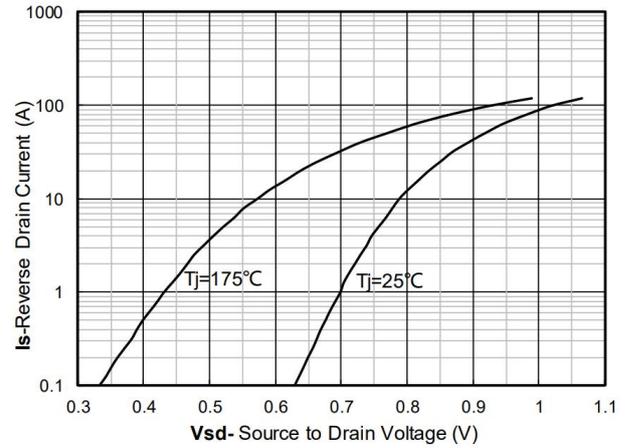


Figure 8. Forward characteristics of reverse diode

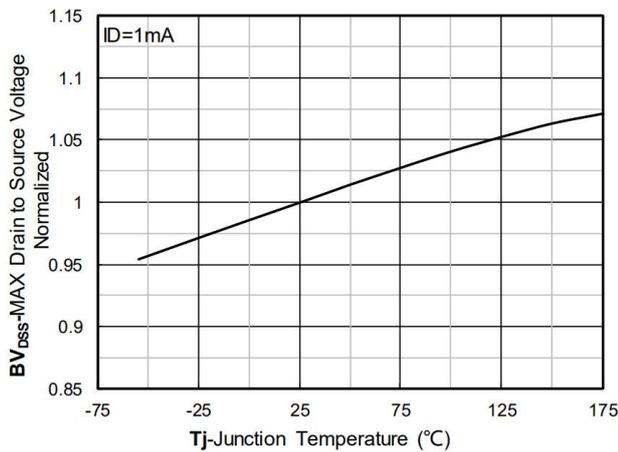


Figure 9. Normalized breakdown voltage

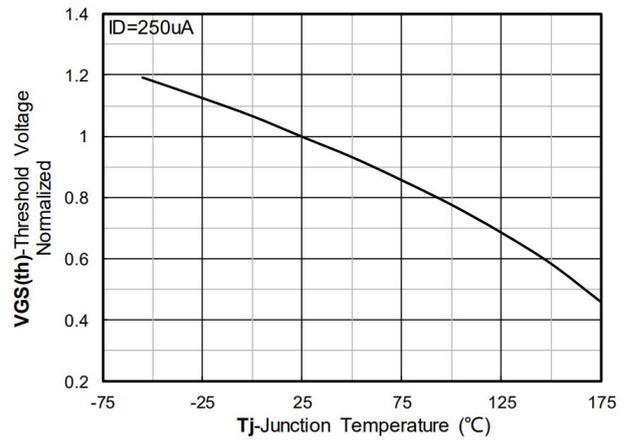


Figure 10. Normalized Threshold voltage

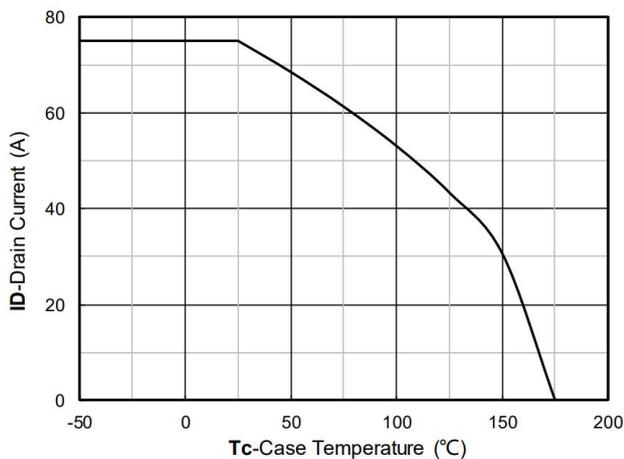


Figure 11. Current dissipation

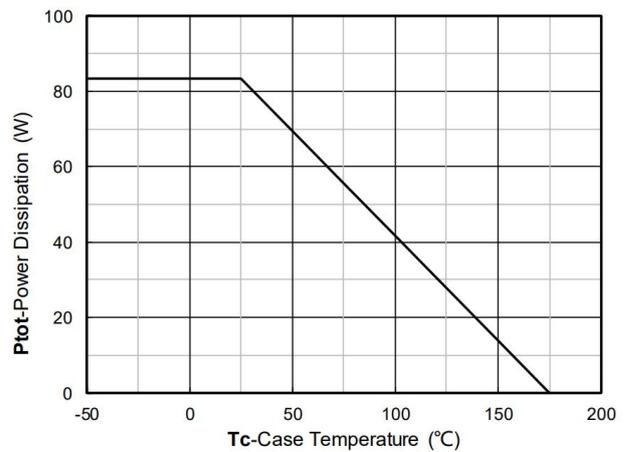


Figure 12. Power dissipation

Typical Characteristics

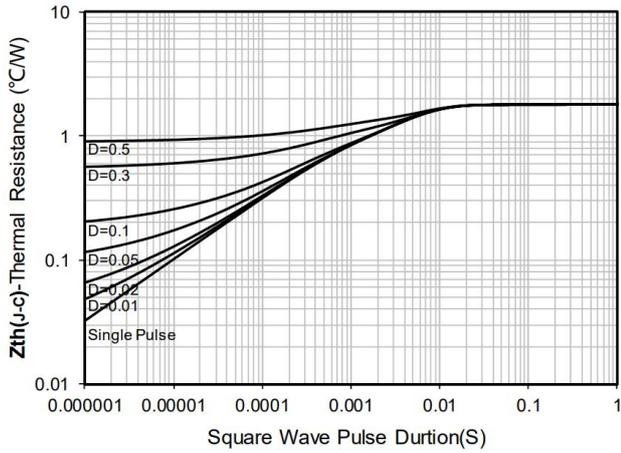


Figure 13. Maximum Transient Thermal Impedance

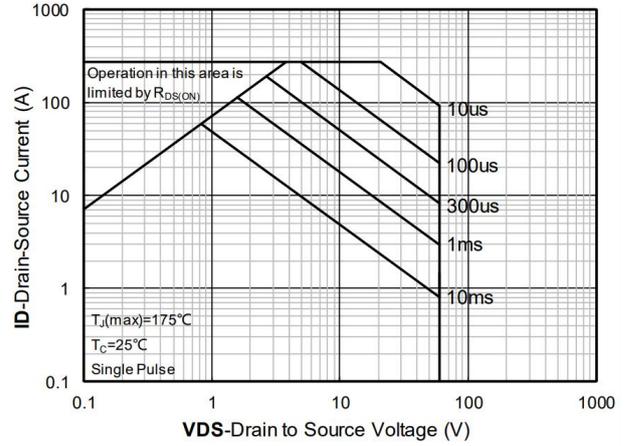
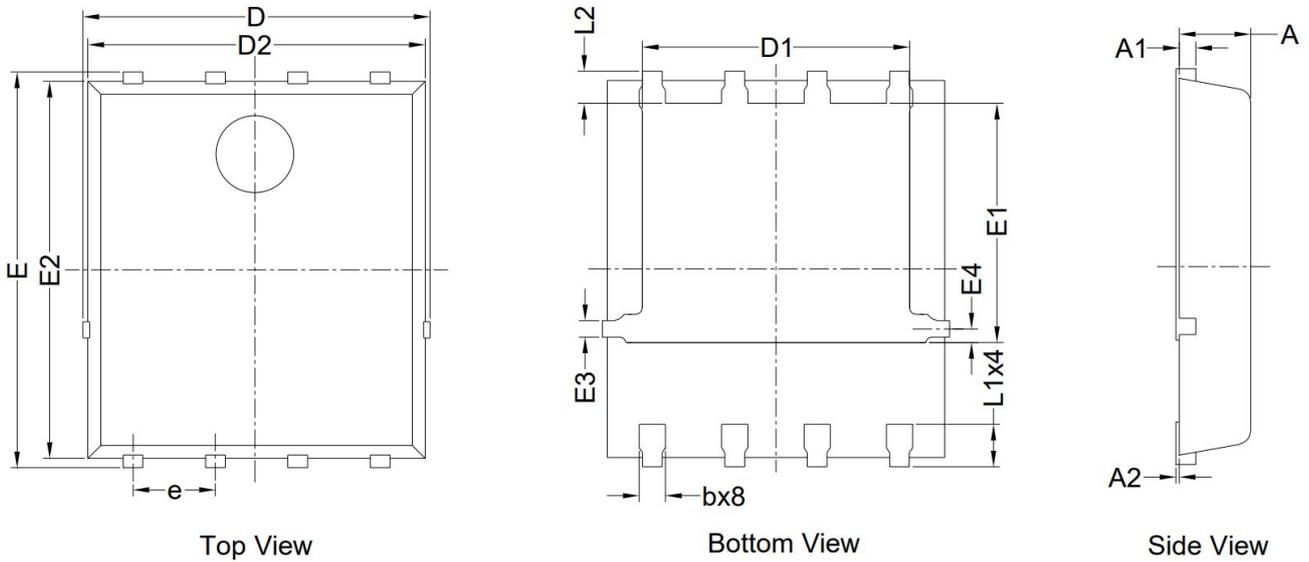


Figure 14. Safe Operation Area

PDFN5*6-8L Package Information



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| D | 5.150 | 5.550 | 0.203 | 0.219 |
| E | 5.950 | 6.350 | 0.234 | 0.250 |
| A | 1.000 | 1.200 | 0.039 | 0.047 |
| A1 | 0.254 BSC. | | 0.100 BSC. | |
| A2 | 0.000 | 0.100 | 0.000 | 0.004 |
| D1 | 3.920 | 4.320 | 0.154 | 0.170 |
| E1 | 3.520 | 3.920 | 0.139 | 0.154 |
| D2 | 5.000 | 5.400 | 0.197 | 0.213 |
| E2 | 5.660 | 6.060 | 0.223 | 0.239 |
| E3 | 0.254 REF. | | 0.010 REF. | |
| E4 | 0.210 REF. | | 0.008 REF. | |
| L1 | 0.560 | 0.760 | 0.022 | 0.030 |
| L2 | 0.500 BSC. | | 0.015 BSC. | |
| b | 0.310 | 0.510 | 0.012 | 0.020 |
| e | 1.270 BSC. | | 0.050 BSC. | |