

### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_D$	$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_D$
100V	27mΩ@10V	5A	-100V	110mΩ@-10V	-3A
	30mΩ@4.5V			120mΩ@-4.5V	

### Feature

- Trench Power LV MOSFET technology
- Excellent package for heat dissipation
- Suffix "-Q1" for AEC-Q101

### Application

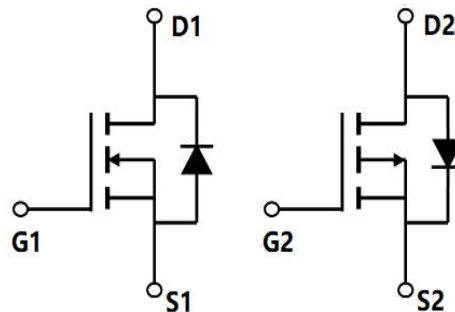
- Load switching
- Hard switched and high frequency circuits
- Uninterruptible power supply

### Package



SOP-8

### Circuit diagram



### Marking



### Absolute maximum ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	$V_{DS}$	100	-100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current	$I_D$	5	-3	A
Continuous Drain Current ( $T_A=100^\circ\text{C}$ )	$I_D(100^\circ\text{C})$	3.2	-1.9	
Pulsed Drain Current <sup>1)</sup>	$I_{DM}$	30	-25	A
Power Dissipation <sup>2)</sup>	$P_D$	1.47	1.3	W
Thermal Resistance Junction to Ambient <sup>3)</sup>	$R_{\theta JA}$	85	90	$^\circ\text{C}/\text{W}$
Operating Junction Temperature	$T_J$	-55 ~ +150	-55 ~ +150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 ~ +150	-55 ~ +150	$^\circ\text{C}$

### N-CH Electrical characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}$			1	$\mu\text{A}$
Gate-body leakage current	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.8	2.5	V
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}, I_D = 5\text{A}$		21	27	m $\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 2\text{A}$		24	30	
<b>Dynamic characteristics<sup>4)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 50\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		1170		pF
Output Capacitance	$C_{oss}$			370		
Reverse Transfer Capacitance	$C_{rss}$			15		
Total Gate Charge	$Q_g$	$V_{DS} = 50\text{V}, V_{GS} = 10\text{V}, I_D = 5\text{A}$		16		nC
Gate-Source Charge	$Q_{gs}$			5.6		
Gate-Drain Charge	$Q_{gd}$			2.4		
Turn-on delay time	$t_{d(on)}$	$V_{DS} = 50\text{V}, V_{GS} = 10\text{V}, I_D = 5\text{A}$ $R_G = 2.2\Omega$		39.2		nS
Turn-on rise time	$t_r$			11		
Turn-off delay time	$t_{d(off)}$			53.2		
Turn-off fall time	$t_f$			15.8		
<b>Source-Drain Diode characteristics</b>						
Diode Forward Current	$I_S$				5	A
Diode Forward voltage	$V_{SD}$	$V_{GS} = 0\text{V}, I_S = 5\text{A}$			1.2	V
Reverse recover time	$T_{rr}$	$I_F = 5\text{A}, di/dt = -100\text{A}/\mu\text{s}$		39.8		nS
Reverse recovery charge	$Q_{rr}$			42		nC

### P-CH Electrical characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-100			V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V			-1	μA
Gate-body leakage current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1	-1.7	-2.5	V
Drain-source on-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3A		85	110	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A		95	120	
<b>Dynamic characteristics<sup>4)</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-50V, V <sub>GS</sub> =0V, f =1MHz		1050		pF
Output Capacitance	C <sub>oss</sub>			110		
Reverse Transfer Capacitance	C <sub>rss</sub>			10		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-50V, V <sub>GS</sub> =-10V I <sub>D</sub> =-3A		20.1		nC
Gate-Source Charge	Q <sub>gs</sub>			3.9		
Gate-Drain Charge	Q <sub>gd</sub>			4.3		
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DS</sub> =-50V, V <sub>GS</sub> =-10V I <sub>D</sub> =-3A, R <sub>G</sub> =2.2Ω		10		nS
Turn-on rise time	t <sub>r</sub>			30		
Turn-off delay time	t <sub>d(off)</sub>			77		
Turn-off fall time	t <sub>f</sub>			81		
<b>Source-Drain Diode characteristics</b>						
Diode Forward Current	I <sub>S</sub>				-3	A
Diode Forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-3A			-1.2	V
Reverse recover time	T <sub>rr</sub>	I <sub>F</sub> =-3A, di/dt =-100A/us		70		nS
Reverse recovery charge	Q <sub>rr</sub>				140	

Notes:

- 1) Repetitive rating; pulse width limited by max. junction temperature.
- 2) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 3) The value of R<sub>θJA</sub> is measured with the device mounted on the the minimum recommend pad size, in the still air environment with T<sub>A</sub>=25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 4) Guaranteed by design, not subject to production testing.

## N-Channel Typical Characteristics

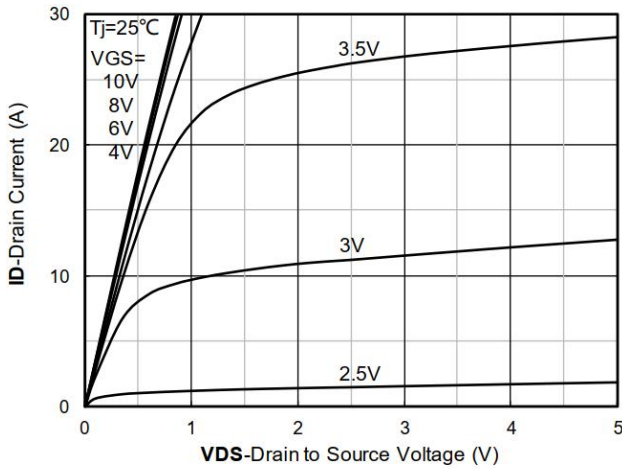


Figure 1. Output Characteristics

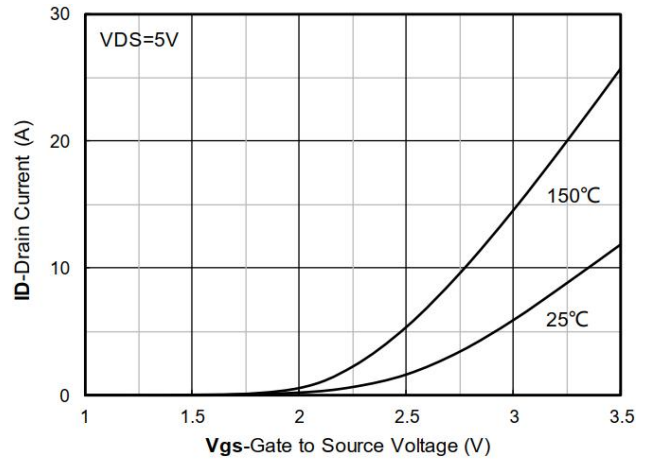


Figure 2. Transfer Characteristics

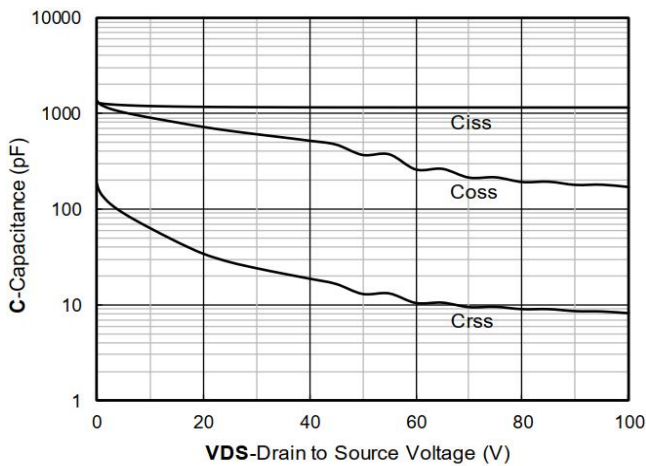


Figure 3. Capacitance Characteristics

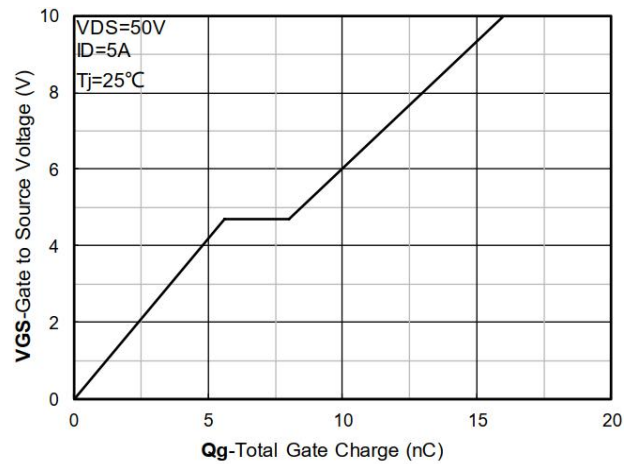


Figure 4. Gate Charge

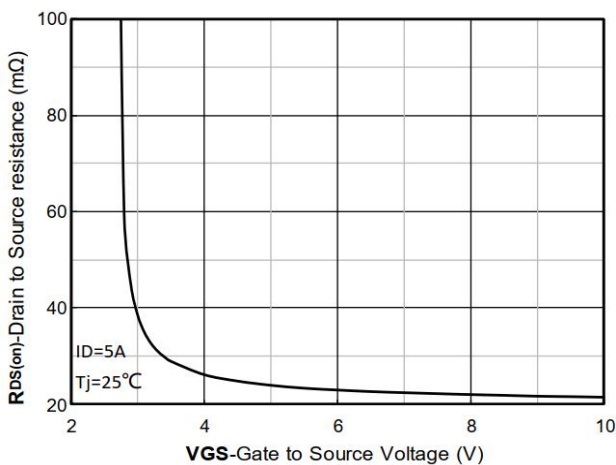


Figure 5. On-Resistance vs Gate to Source Voltage

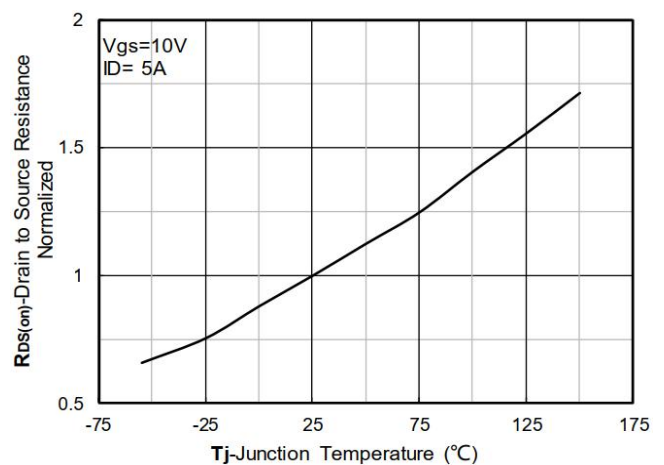


Figure 6. Normalized On-Resistan

## N-Channel Typical Characteristics

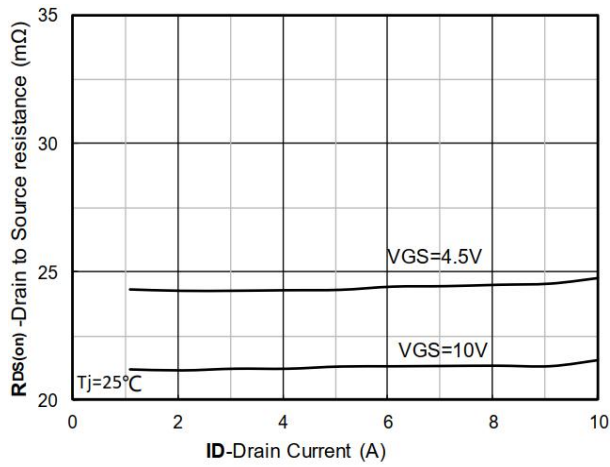


Figure 7.  $R_{DS(on)}$  VS Drain Current

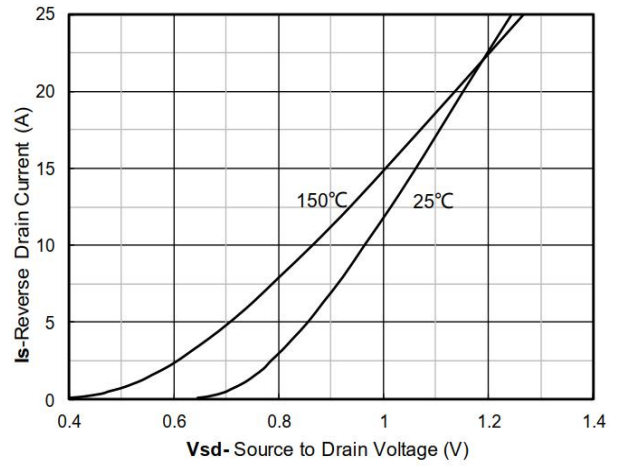


Figure 8. Forward characteristics of reverse diode

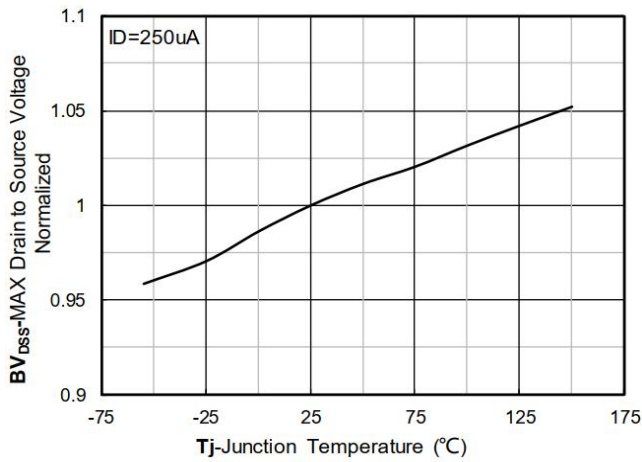


Figure 9. Normalized breakdown voltage

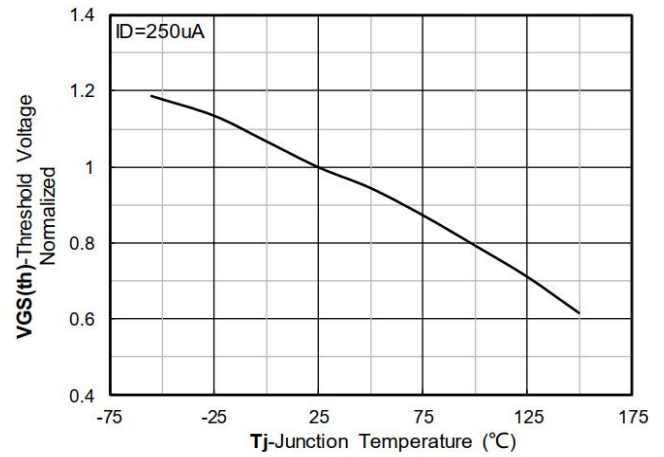


Figure 10. Normalized Threshold voltage

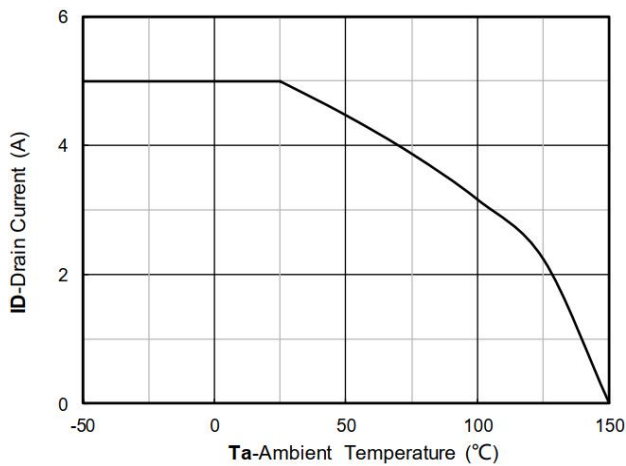


Figure 11. Current dissipation

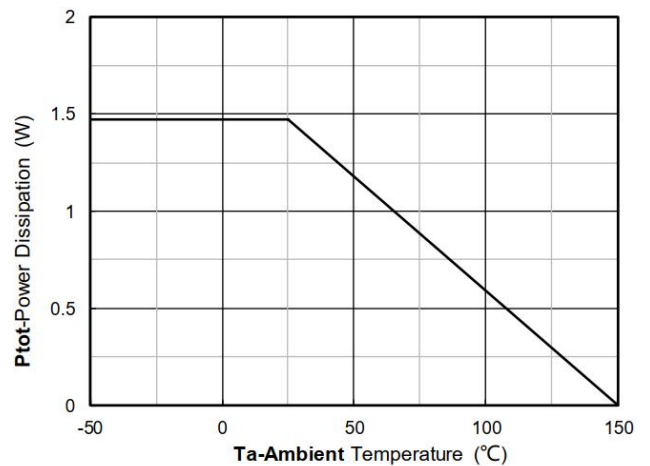


Figure 12. Power dissipation

## N-Channel Typical Characteristics

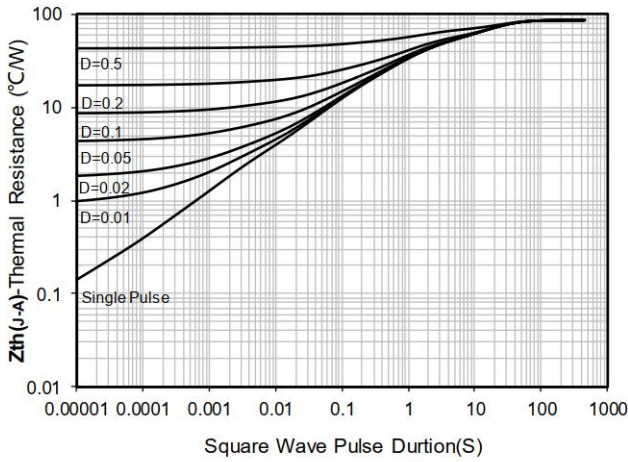


Figure 13. Maximum Transient Thermal Impedance

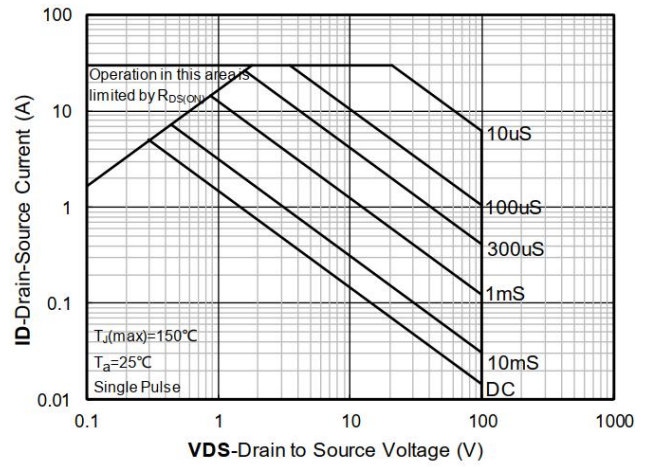


Figure 14. Safe Operation Area

## P-Channel Typical Characteristics

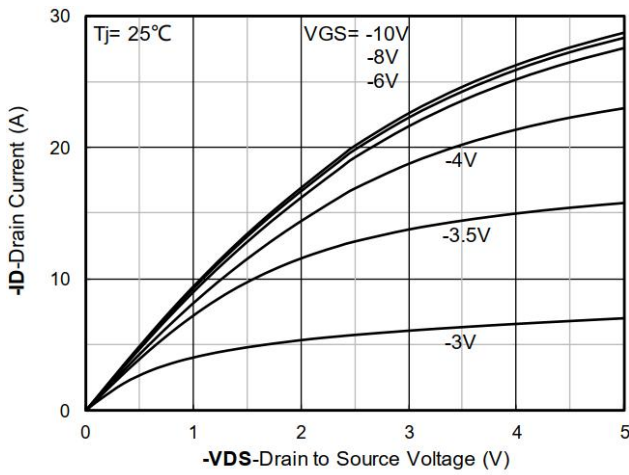


Figure 1. Output Characteristics

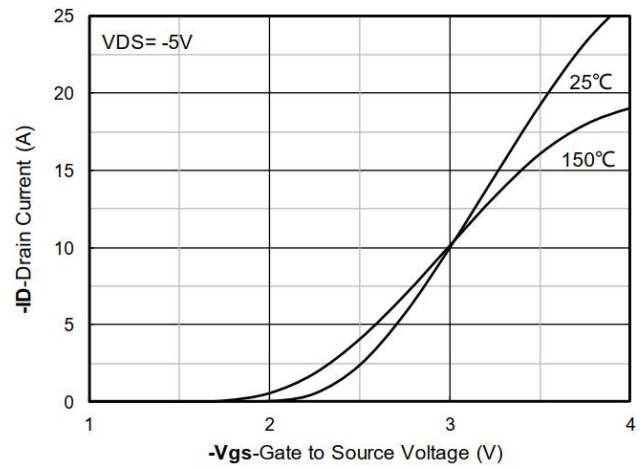


Figure 2. Transfer Characteristics

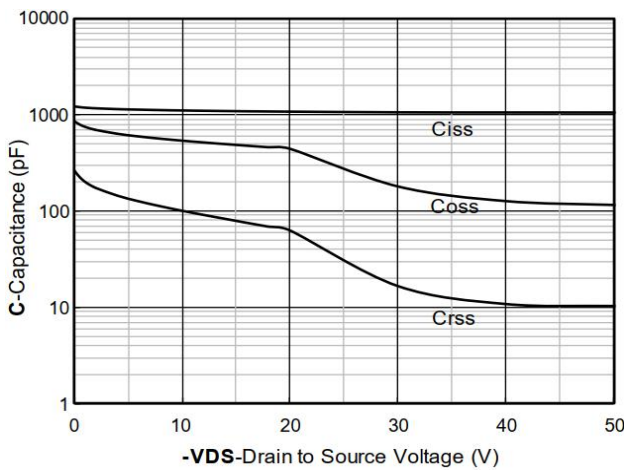


Figure 3. Capacitance Characteristics

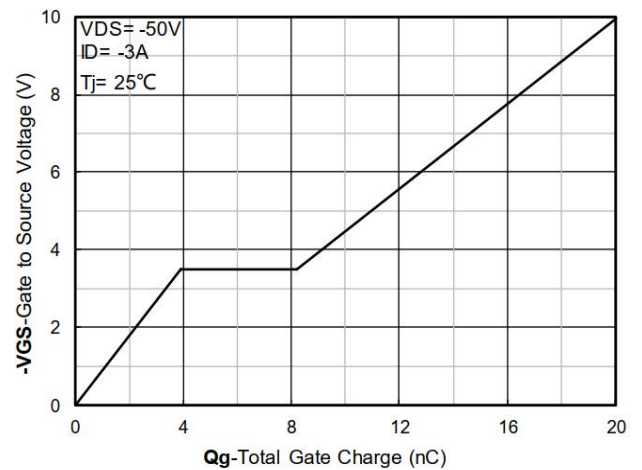


Figure 4. Gate Charge

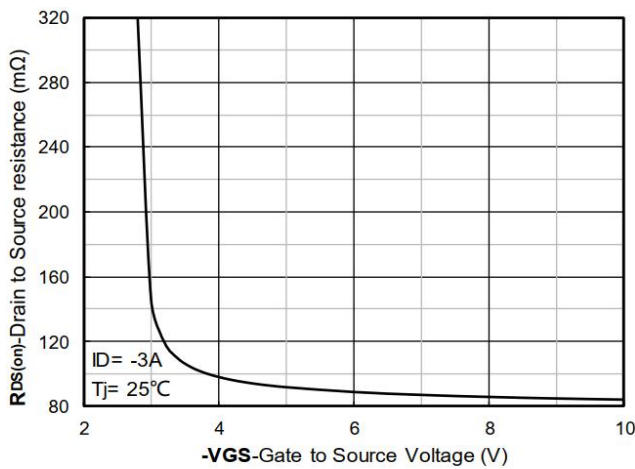


Figure 5. On-Resistance vs Gate to Source Voltage

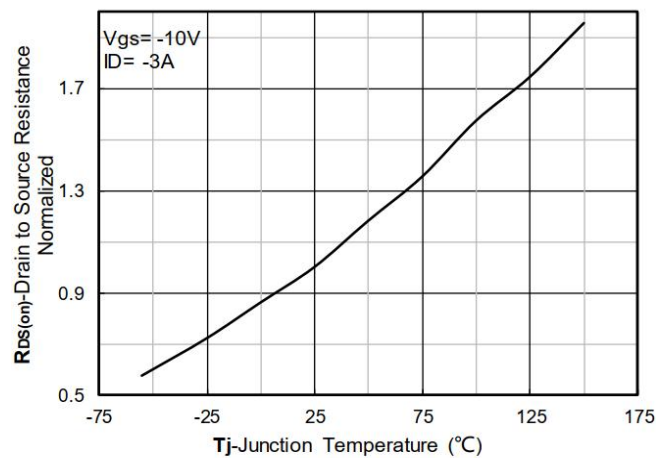


Figure 6. Normalized On-Resistance

## P-Channel Typical Characteristics

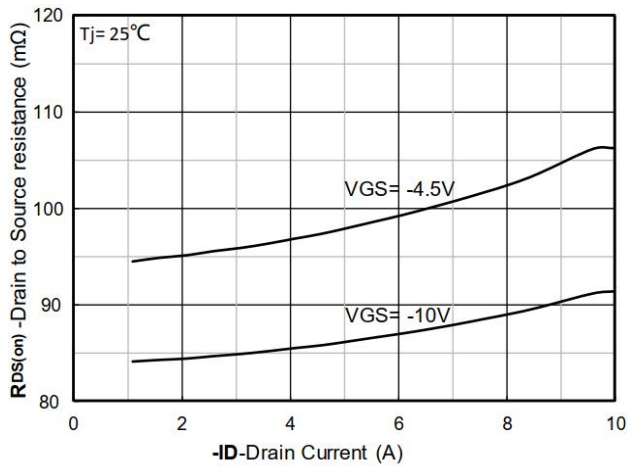


Figure 7.  $R_{DS(on)}$  VS Drain Current

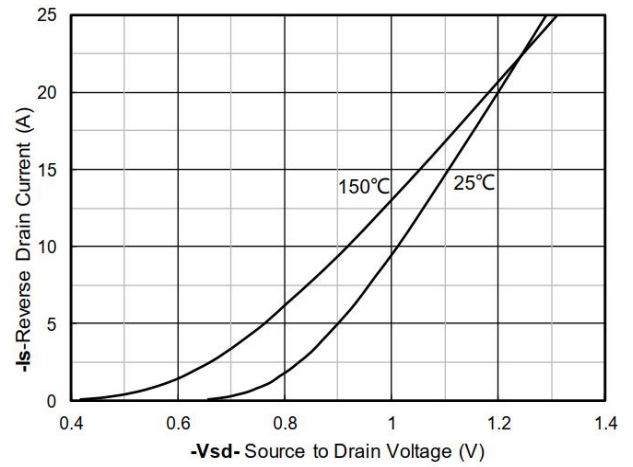


Figure 8. Forward characteristics of reverse diode

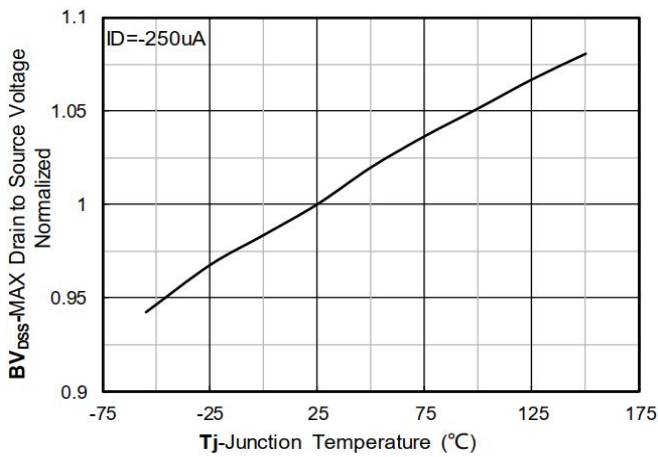


Figure 9. Normalized breakdown voltage

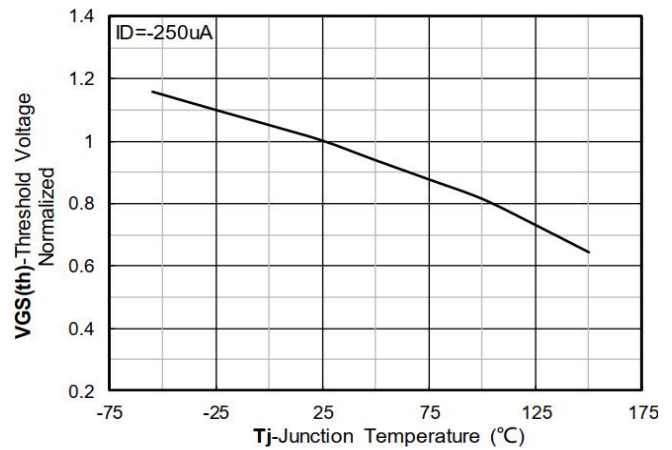


Figure 10. Normalized Threshold voltage

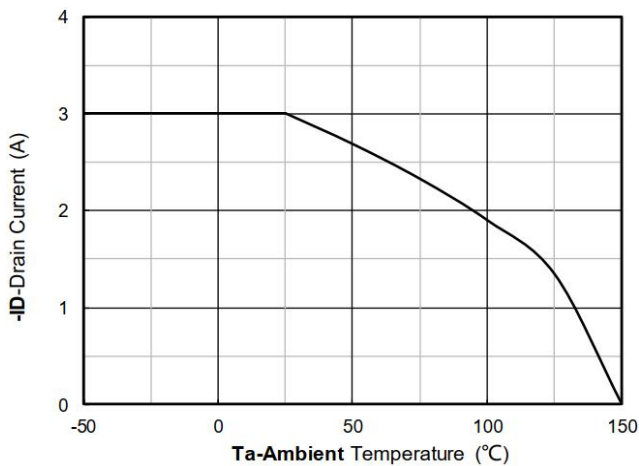


Figure 11. Current dissipation

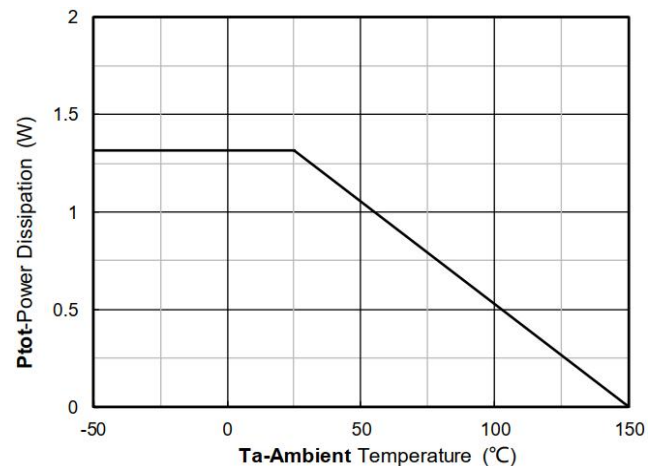


Figure 12. Power dissipation

## P-Channel Typical Characteristics

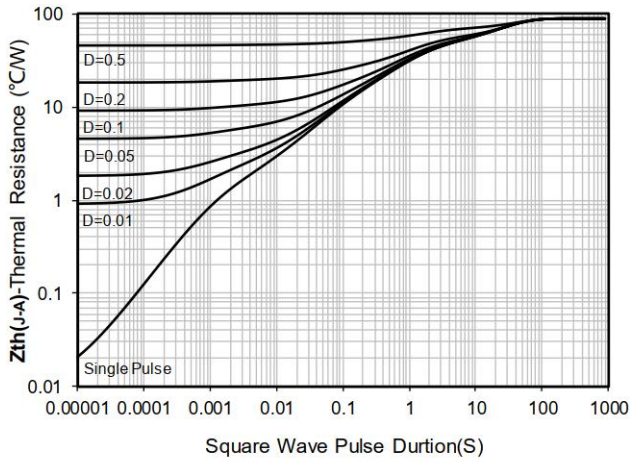


Figure 13. Maximum Transient Thermal Impedance

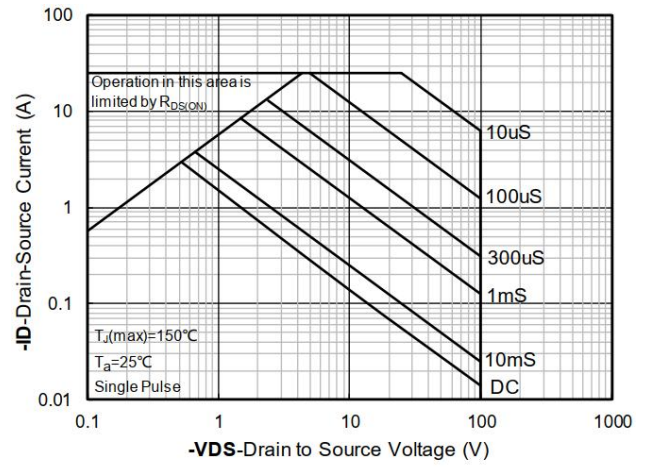
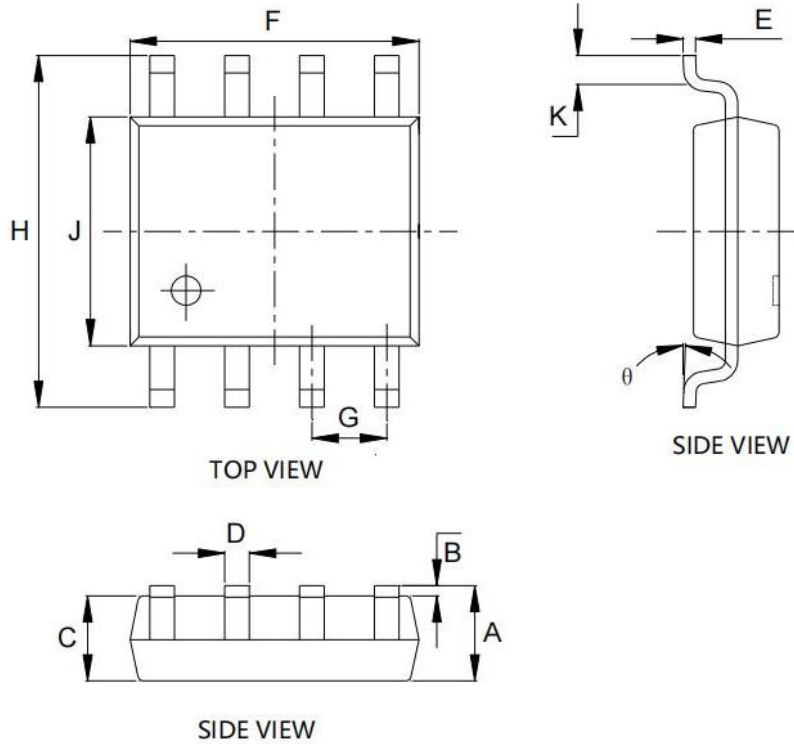


Figure 14. Safe Operation Area

### SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
B	0.100	0.250	0.004	0.010
C	1.350	1.550	0.053	0.061
D	0.330	0.510	0.013	0.020
E	0.170	0.250	0.007	0.010
F	4.800	5.000	0.189	0.197
G	1.270 BSC.		0.050 BSC.	
H	5.800	6.200	0.228	0.244
J	3.800	4.000	0.150	0.157
K	0.400	1.270	0.016	0.050
$\theta$	0°	8°	0°	8°