

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
650V	330mΩ@10V	11A

Feature

- Optimized body diode reverse recovery performance
- Low on-resistance and low conduction losses
- Ultra Low Gate Charge cause lower driving requirements
- ESD protected

Application

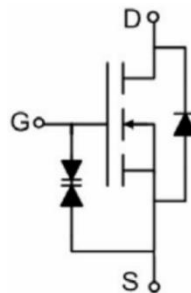
- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible power supply (UPS)
- LLC Half-bridge

Package

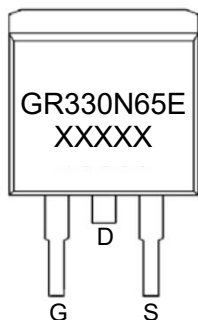


TO-263AB

Circuit Diagram



Marking



Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	650	V
Gate-Source Voltage AC(f > 1Hz)	V _{GS}	±30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	11	A
Continuous Drain Current(T _C =100°C)	I _D (100°C)	7.7	A
Pulsed Drain Current ¹⁾	I _{DM}	44	A
Power Dissipation	P _D	107	W
Thermal Resistance,Junction-to-Case	R _{θJC}	1.4	°C/W
Thermal Resistance from Junction to Ambient	R _{θJA}	62	°C/W
Avalanche Current ²⁾	I _{AS}	3	A
Junction Temperature	T _J	175	°C
Storage Temperature	T _{STG}	-55 ~ +175	°C

Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250μA	650			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =650V, V _{GS} = 0V, T _C =25°C			1	μA
		V _{DS} =650V, V _{GS} = 0V, T _C =125°C			100	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} = 0V			±10	μA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	3.0	3.5	4.0	V
Drain-Source on-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =5.5A		300	330	mΩ
Dynamic Characteristics³⁾						
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, f =1MHz		847		pF
Output Capacitance	C _{oss}			31		
Reverse Transfer Capacitance	C _{rss}			4		
Gate Resistance	R _g	f =1MHz, open drain		18		Ω
Total Gate Charge	Q _g	V _{DS} =480V, V _{GS} =10V, I _D =5.5A		17		nC
Gate-Source Charge	Q _{gs}			4.4		
Gate-Drain Charge	Q _{gd}			4.9		
Gate Plateau Volitage	V _{gp}			5.4		
Turn-on Delay Time	t _{d(on)}	V _{DD} =480V, V _{GS} =10V, I _D =5.5A, R _G =1.7Ω		10		nS
Turn-on Rise Time	t _r			7		
Turn-off Delay Time	t _{d(off)}			55		
Turn-off Fall Time	t _f			8		
Source-Drain Diode Characteristics						
Diode Forward Current	I _{SD}	T _C =25°C			11	A
Diode Forward Pulse Current	I _{SDM}				44	A
Diode Forward voltage	V _{SD}	T _J =25°C, V _{GS} =0V, I _{SD} =11A			1.2	V
Reverse Recovery Time	t _{rr}	T _J =25°C, I _F =5.5A, di/dt=100A/μs		200		nS
Reverse Recovery Charge	Q _{rr}			1.6		uC
Peak Reverse Recovery Current	I _{rrm}			16		A

Notes:

- 1) Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2) T_J=25 °C, V_{DD}=50V, V_G=10V, R_G=25Ω.
- 3) Guaranteed by design, not subject to production.

Typical Characteristics

Figure1. Safe operating area

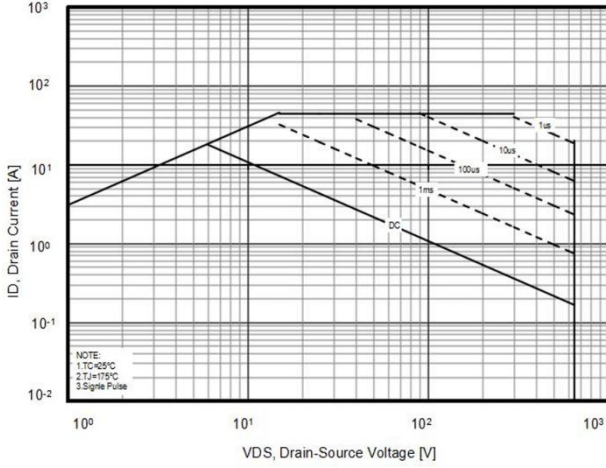


Figure2. Capacitance

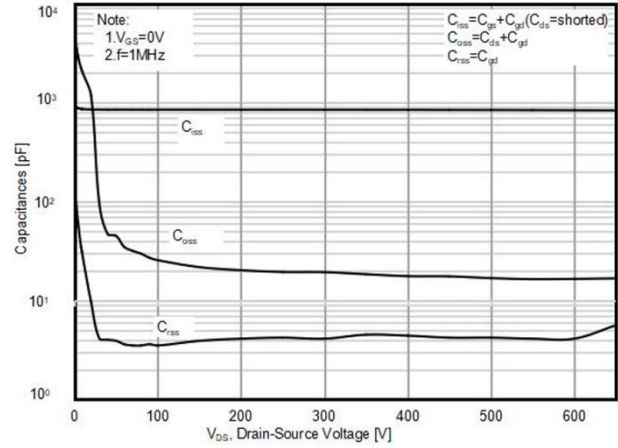


Figure3. Transfer characteristics

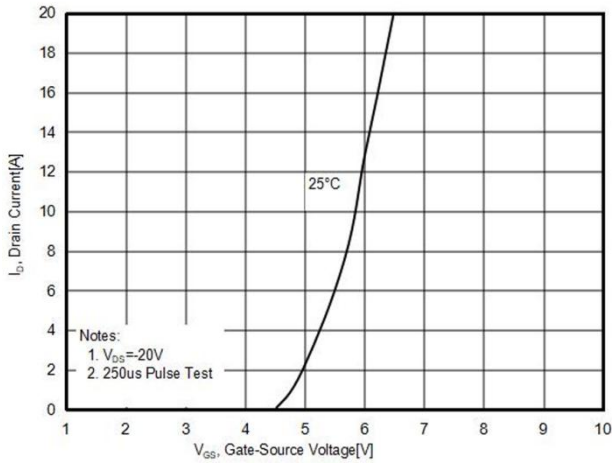


Figure4. Output characteristics

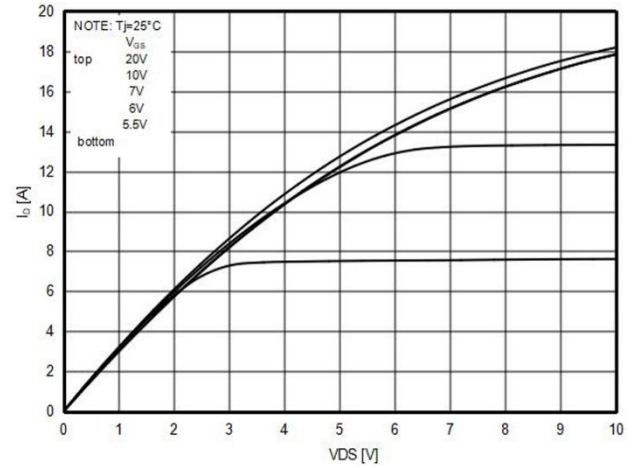


Figure5. RDS(ON) vs Junction Temperature

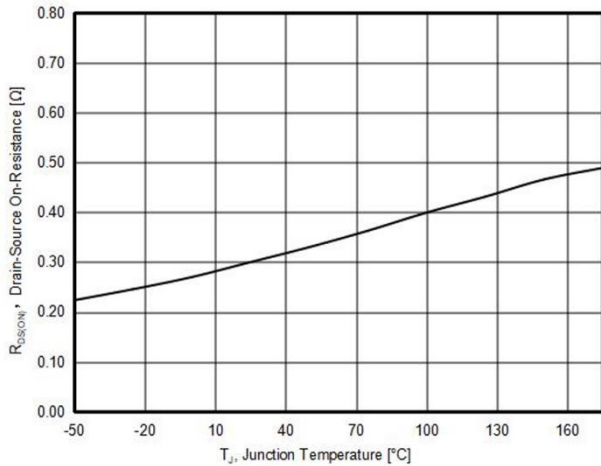
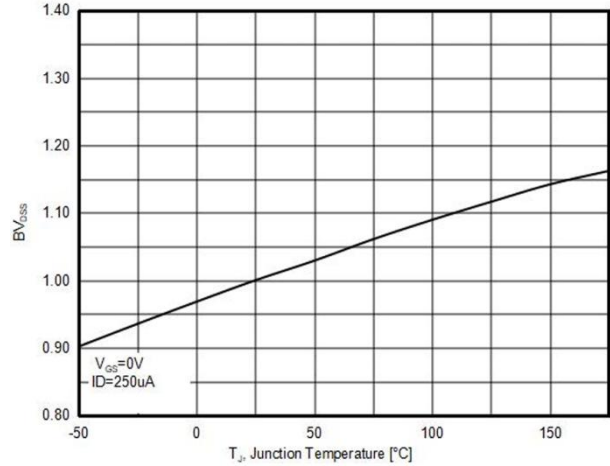


Figure6. BVDS vs Junction Temperature



Typical Characteristics

Figure7. Maximum I_D vs Junction Temperature

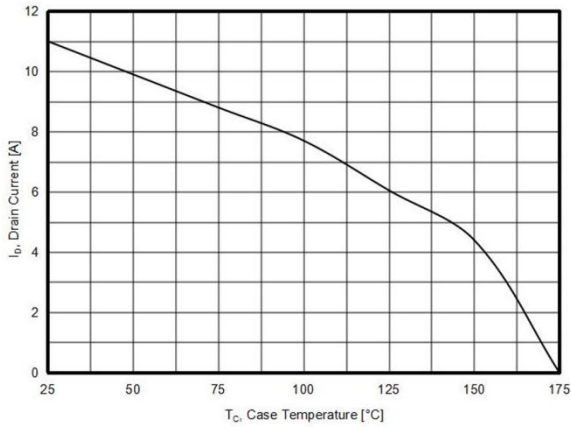


Figure8. Gate charge waveforms

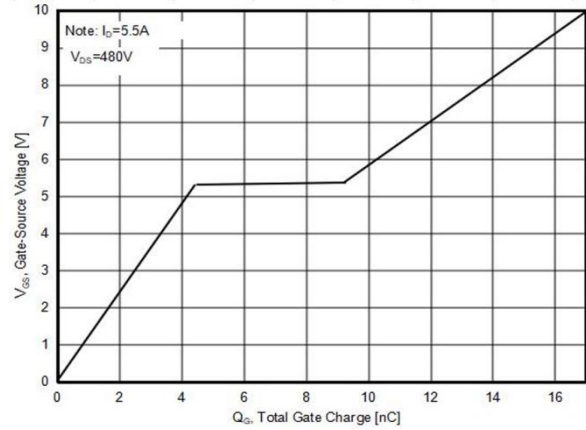


Figure9. Static drain-source on resistance

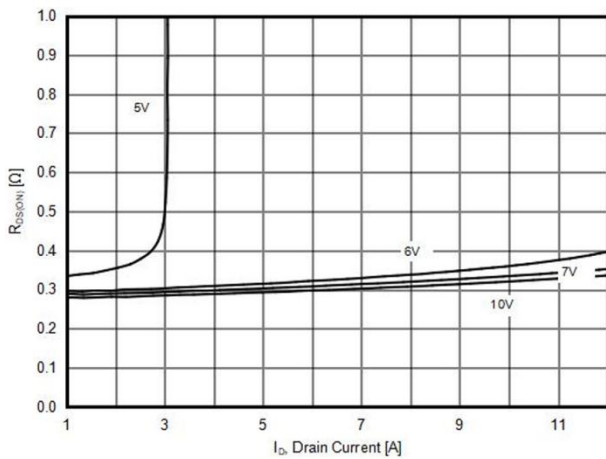
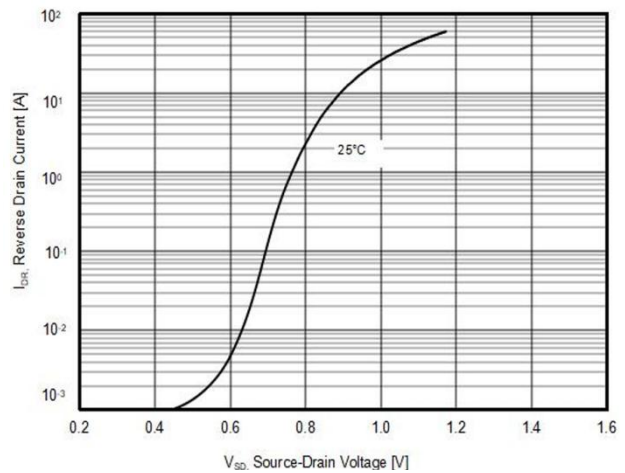
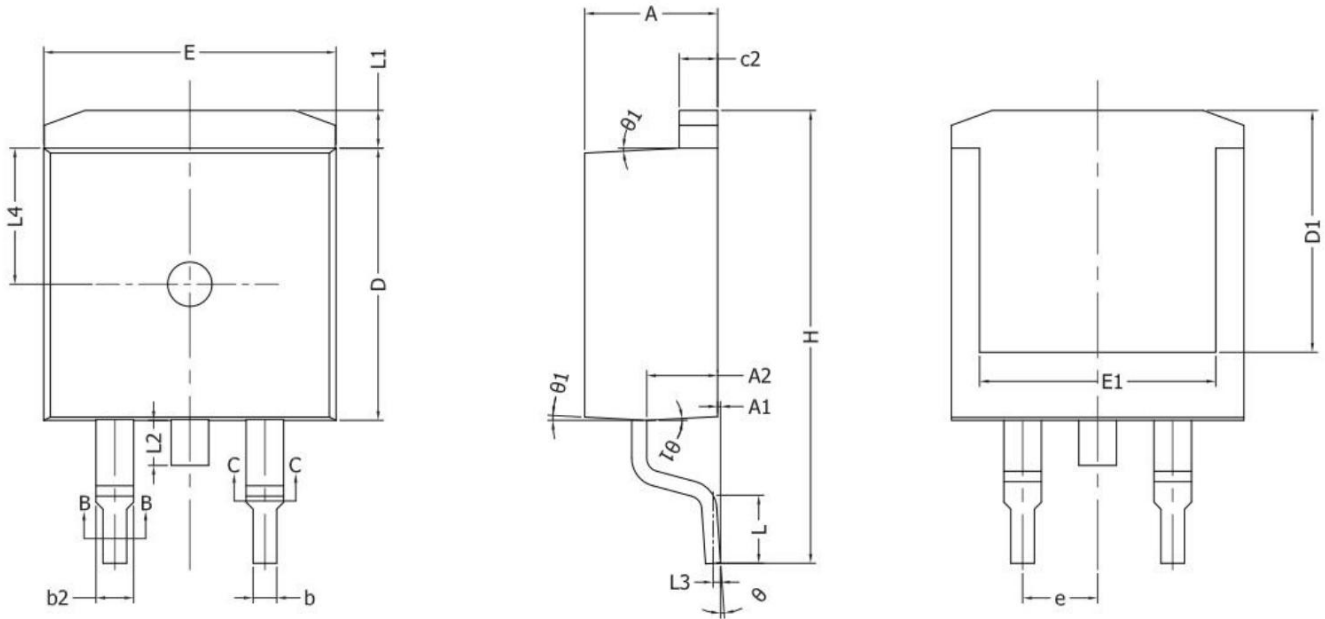


Figure10. Source-Drain Diode Forward Voltage



TO-263AB Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	0.000	0.250	0.000	0.010
A2	2.200	2.600	0.087	0.102
b	0.760	0.890	0.030	0.035
b1	0.750	0.850	0.030	0.033
b2	1.230	1.370	0.048	0.054
b3	1.220	1.320	0.048	0.052
c	0.470	0.600	0.019	0.024
c1	0.460	0.560	0.018	0.022
c2	1.250	1.350	0.049	0.053
D	9.100	9.300	0.358	0.366
D1	8.000	-	0.315	-
E	9.800	10.000	0.386	0.394
E1	7.800	-	0.307	-
e	2.540BSC		0.100BSC	
H	14.900	15.700	0.587	0.618
L	2.000	2.600	0.079	0.102
L1	1.170	1.400	0.046	0.055
L2		1.750		0.069
L3	0.250REF		0.010REF	
L4	4.600REF		0.181REF	