

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
30V	132mΩ@10V	1.4A
	144mΩ@4.5V	
	185mΩ@2.5V	

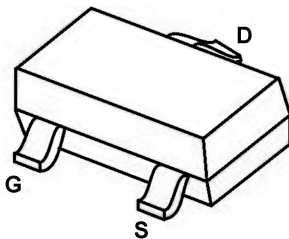
Feature

- High density cell design for ultra low on-resistance
- High Power and current handing capability
- Surface-mounted package
- ESD protected

Application

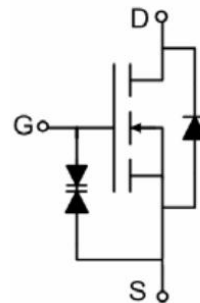
- Smartphone, Tablet
- DC/DC Converter
- Boost Converter
- Load Switch, OVP Switch

Package

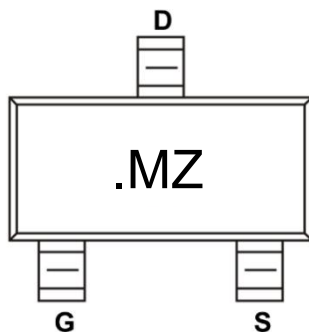


SOT-323

Circuit diagram



Marking



Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±12	V
Continuous Drain Current	I _D	1.4	A
Pulsed Drain Current ¹⁾	I _{DM}	6	A
Power Dissipation ²⁾	P _D	0.4	W
Thermal Resistance from Junction to Ambient ²⁾	R _{θJA}	312	°C/W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

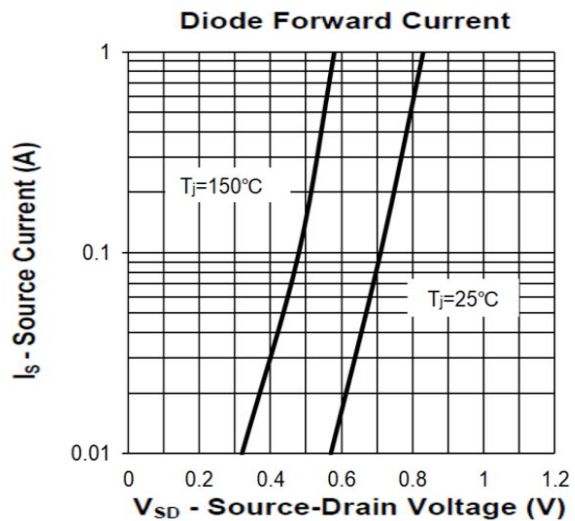
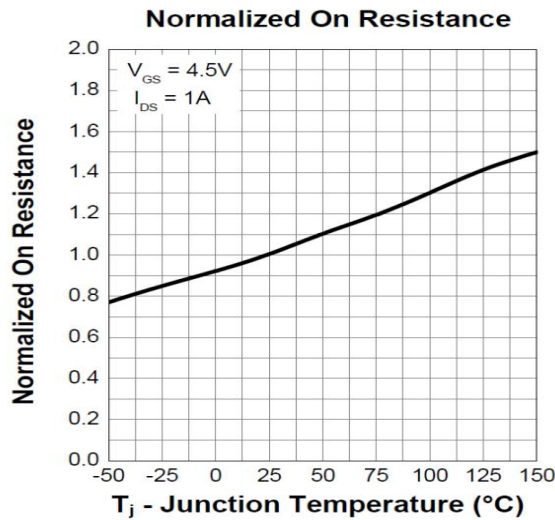
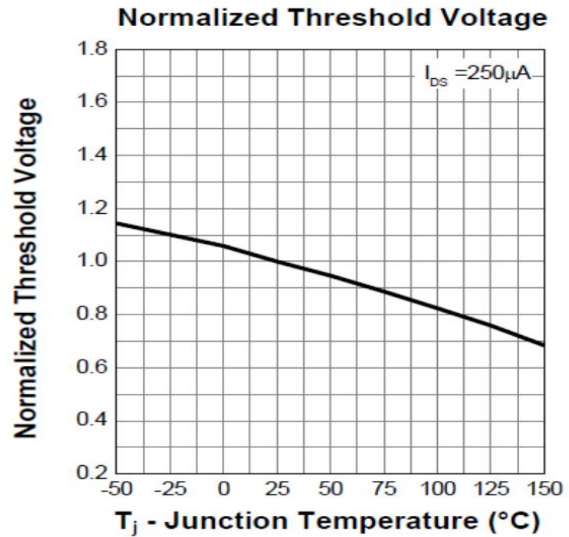
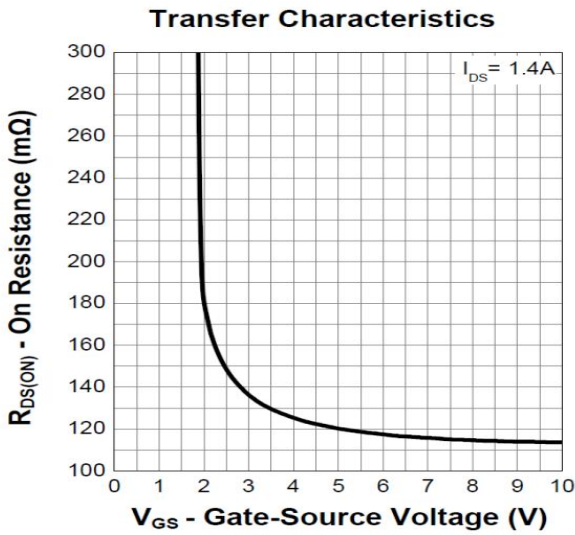
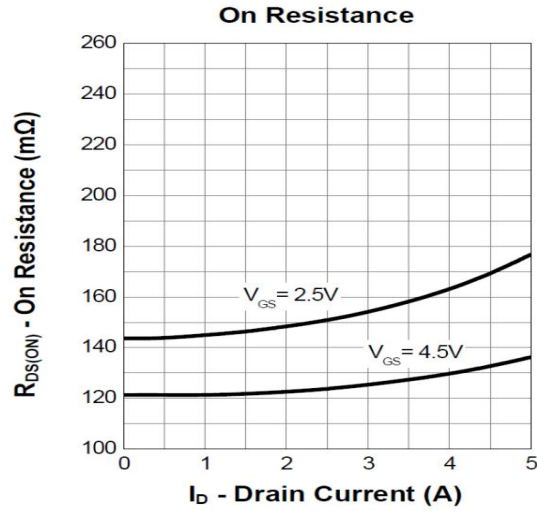
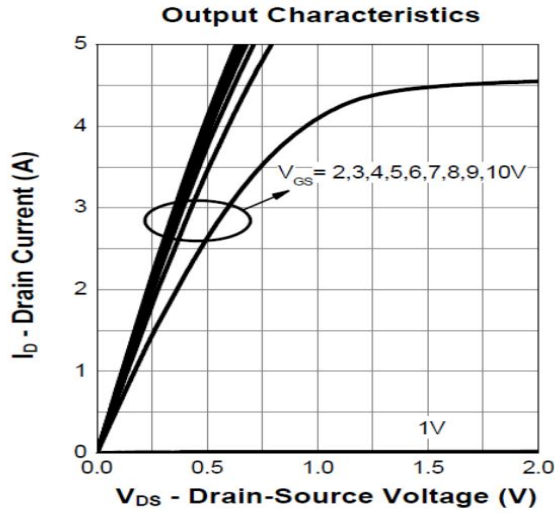
Electrical characteristics (Ta=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V			1	μA
Gate-body leakage current	I _{GSS}	V _{GS} = ±12V, V _{DS} = 0V			±20	μA
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.6		1.5	V
Drain-source on-resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 1.4A			132	mΩ
		V _{GS} = 4.5V, I _D = 1A			144	
		V _{GS} = 2.5V, I _D = 0.5A			185	
Dynamic characteristics³⁾						
Forward Transconductance	g _{FS}	V _{DS} = 10V, I _D = 1.4A		2.4		S
Input Capacitance	C _{iss}	V _{DS} = 15V, V _{GS} = 0V, f = 1MHz		180		pF
Output Capacitance	C _{oss}			18		
Reverse Transfer Capacitance	C _{rss}			16		
Total Gate Charge	Q _g		V _{DS} = 15V, V _{GS} = 4.5V, I _D = 1.4A		2.5	
Gate-Source Charge	Q _{gs}			0.8		
Gate-Drain Charge	Q _{gd}			0.5		
Turn-on delay time	t _{d(on)}	V _{DS} = 15V, V _{GS} = 4.5V, I _D = 1.4A R _G = 4.5Ω		4.1		nS
Turn-on rise time	t _r			4.2		
Turn-off delay time	t _{d(off)}			17		
Turn-off fall time	t _f			5.3		
Source-Drain Diode characteristics						
Diode Forward Current	I _S				1.4	A
Diode Forward voltage	V _{SD}	V _{GS} = 0V, I _S = 1.1A			1.2	V
Reverse Recovery Time	trr	I _S = 1.4A, di/dt = 100A/μs		5.6		nS
Reverse Recovery Charge	Qrr			1.8		nC

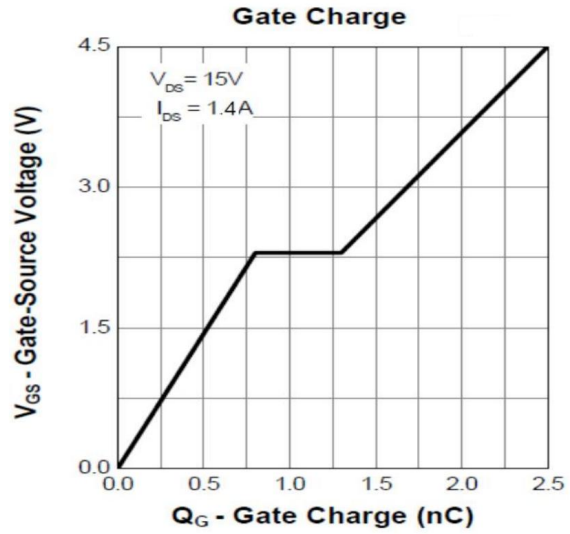
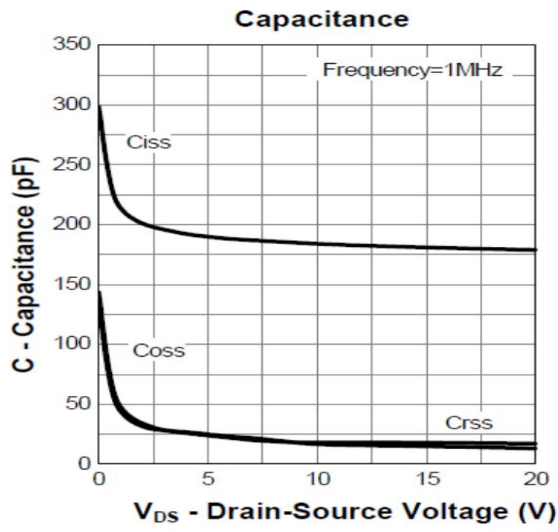
Notes:

- 1) Pulse Test: Pulse Width ≤ 100μs, Duty Cycle ≤ 2%, Repetitive rating, pulse width limited by junction temperature T_{J(MAX)} = 150°C.
- 2) Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.
- 3) Guaranteed by design, not subject to production testing.

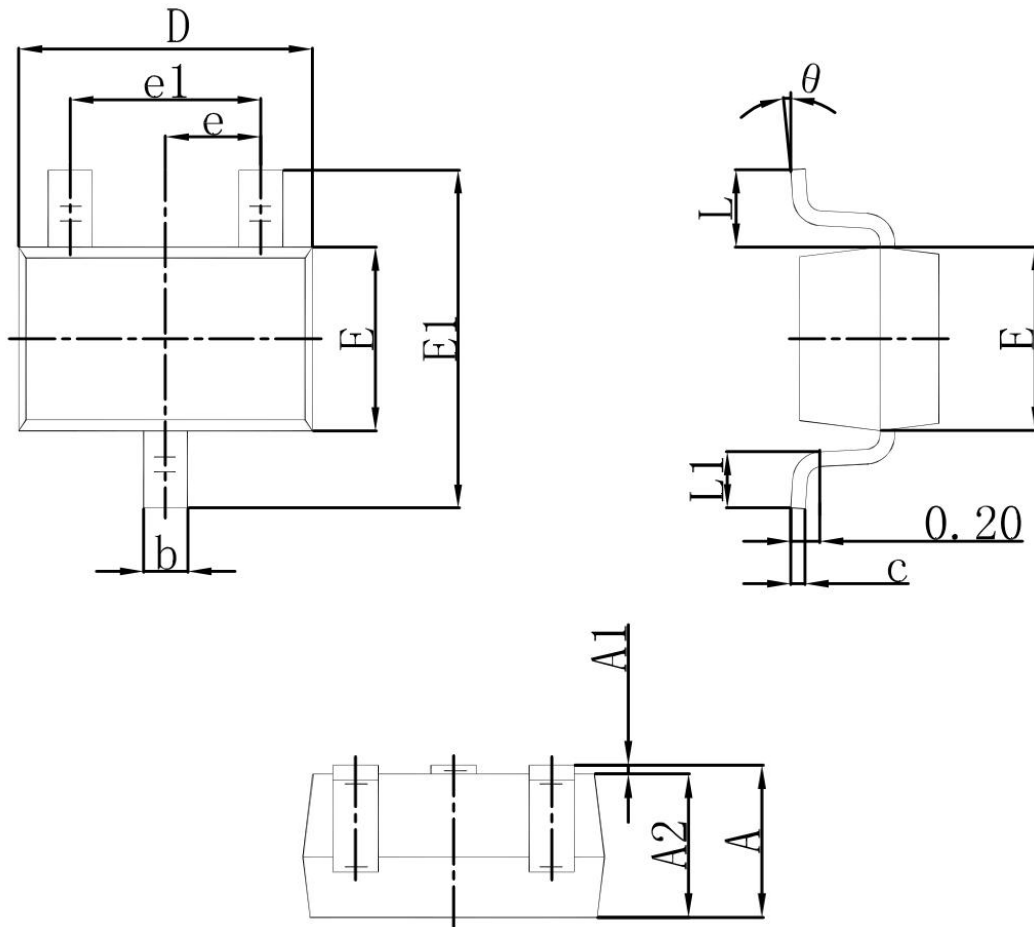
Typical Characteristics



Typical Characteristics



SOT-323 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.800	1.100	0.032	0.043
A1	0.000	0.100	0.000	0.004
A2	0.800	1.000	0.032	0.039
b	0.150	0.400	0.006	0.016
c	0.050	0.250	0.002	0.016
D	1.800	2.200	0.071	0.087
E	1.150	1.350	0.045	0.053
E1	2.000	2.450	0.079	0.096
e	0.650 TYP.		0.026 TYP.	
e1	1.200	1.400	0.047	0.055
L	0.525 REF.		0.021 REF.	
L1	0.200	0.460	0.008	0.018
θ	0°	8°	0°	8°