AS35N10AS-Q1

N-Channel Super Trench Power MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
100V	28mΩ@10V	35A
	32mΩ@4.5V	

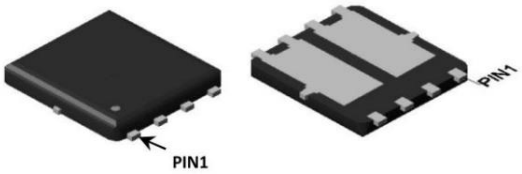
Feature

- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 150 °C operating temperature
- Suffix "-Q1" for AEC-Q101

Application

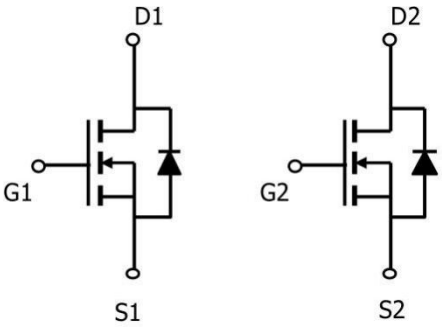
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

Package

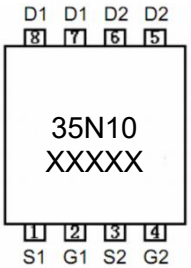


DFN5X6-8L

Circuit diagram



Marking



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Absolute maximum ratings (T_C=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	35	A
Continuous Drain Current(T_C =100℃)	I_D(100℃)	24.5	A
Pulsed Drain Current	I_{DM}	140	A
Power Dissipation	P_D	50	W
Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	2.5	℃/W
Single pulse avalanche energy[1)]	E_{AS}	200	mJ
Junction Temperature	T_J	150	℃
Storage Temperature	T_{STG}	-55 ~ +150	℃

Electrical characteristics (T_C=25℃ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D =250μA	100			V
Zero gate voltage drain current	I_{DSS}	V_{DS} =100V,V_{GS} = 0V			1	μA
Gate-body leakage current	I_{GSS}	V_{GS} =±20V, V_{DS} = 0V			±100	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =250μA	1.2	2.0	2.8	V
Drain-source on-resistance	$R_{DS(on)}$	V_{GS} =10V, I_D =20A		24	28	mΩ
		V_{GS} =4.5V, I_D =20A		27	32	
Dynamic characteristics[2)]						
Input Capacitance	C_{iss}	V_{DS} =50V, V_{GS} =0V, f =1MHz		1600		pF
Output Capacitance	C_{oss}			139		
Reverse Transfer Capacitance	C_{rss}			11		
Total Gate Charge	Q_g	V_{DS} =50V, V_{GS} =10V, I_D =20A		26		nC
Gate-Source Charge	Q_{gs}			7.4		
Gate-Drain Charge	Q_{gd}			3.8		
Turn-on delay time	$t_{d(on)}$	V_{DD}=50V, V_{GS}=10V, I_D=20A, R_G=1.6Ω		6		nS
Turn-on rise time	t_r			2		
Turn-off delay time	$t_{d(off)}$			18		
Turn-off fall time	t_f			2		
Source-Drain Diode characteristics						
Diode Forward Current	I_S				35	A
Diode Forward voltage	V_{SD}	V_{GS} =0V, I_S=35A			1.2	V
Reverse Recovery Time	t_{rr}	T_J = 25°C, I_F = 20A di/dt = 500A/μs		26		nS
Reverse Recovery Charge	Q_{rr}			98		nC

Notes:

1) EAS condition : Tj=25℃,VDD=20V,VG=10V,L=0.5mH,Rg=25Ω

2) Guaranteed by design, not subject to production

3) These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of TJ(MAX)=150°C. The SOA curve provides a single pulse rating.

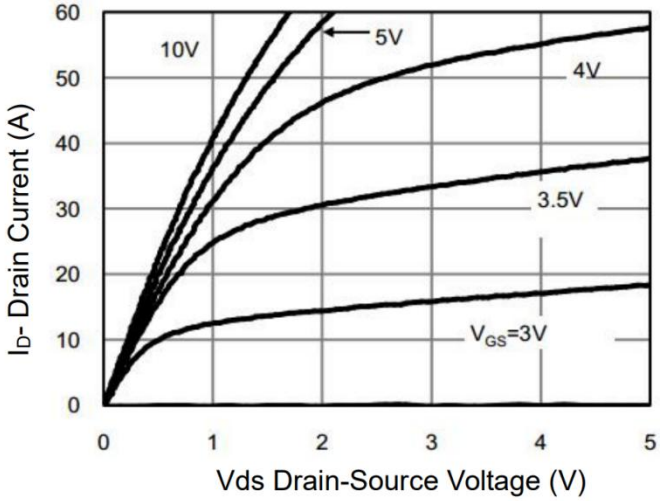


Figure 1 Output Characteristics

Typical Characteristics

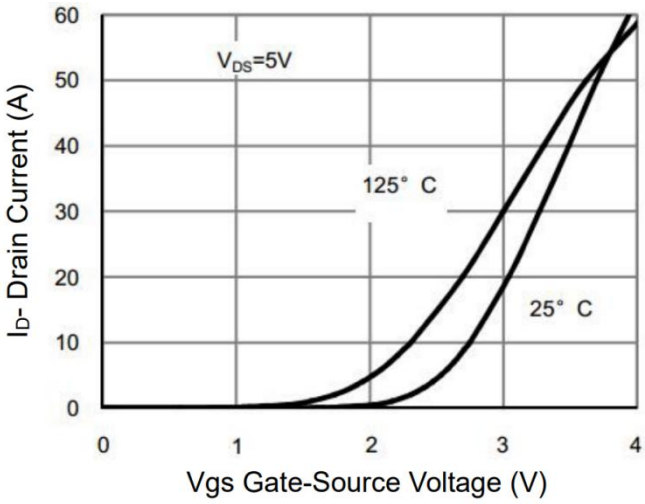


Figure 2 Transfer Characteristics

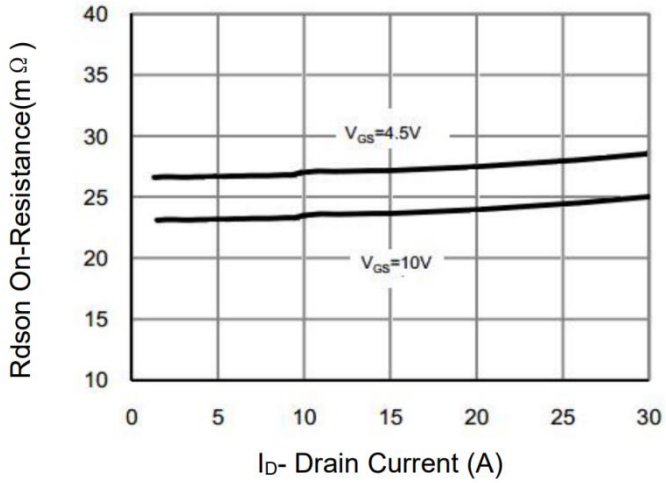


Figure 3 Rdson- Drain Current

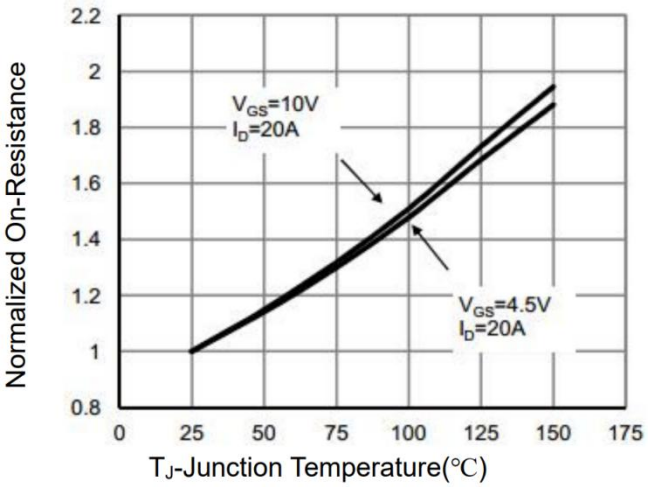


Figure 4 Rdson-Junction Temperature

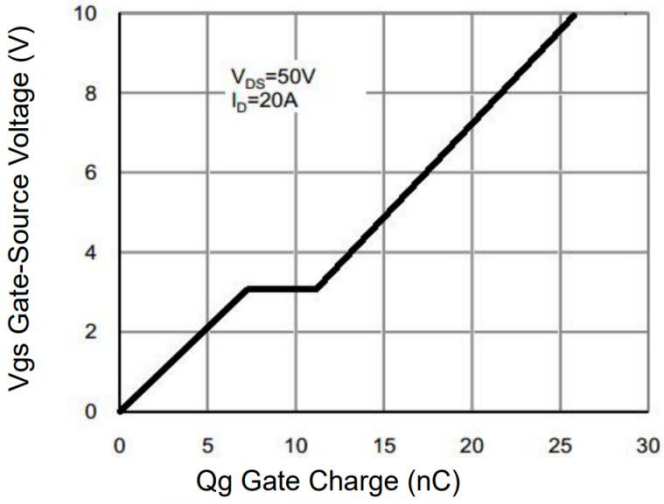


Figure 5 Gate Charge

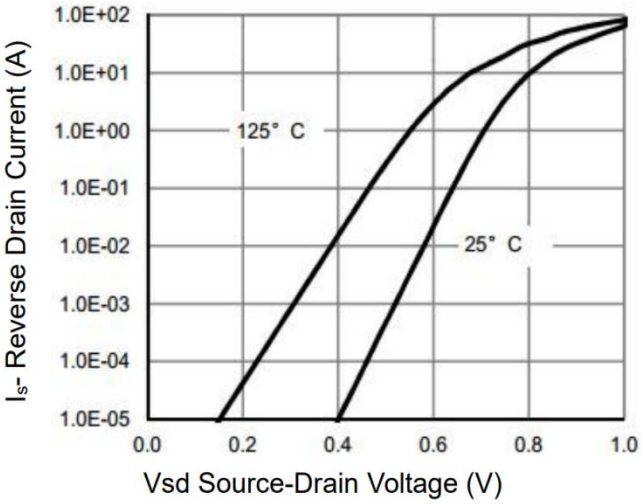


Figure 6 Source- Drain Diode Forward

Typical Characteristics

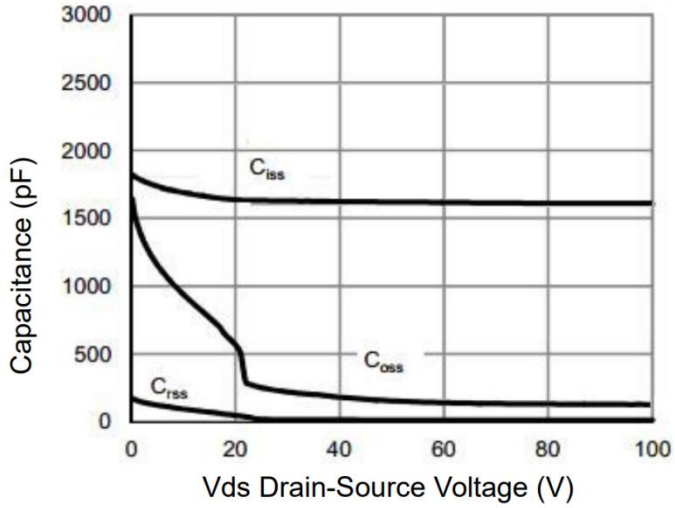


Figure 7 Capacitance vs Vds

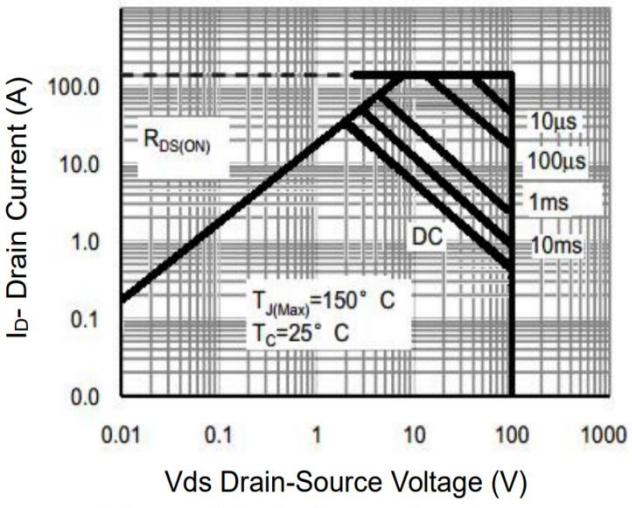


Figure 8 Safe Operation Area(Note 3)

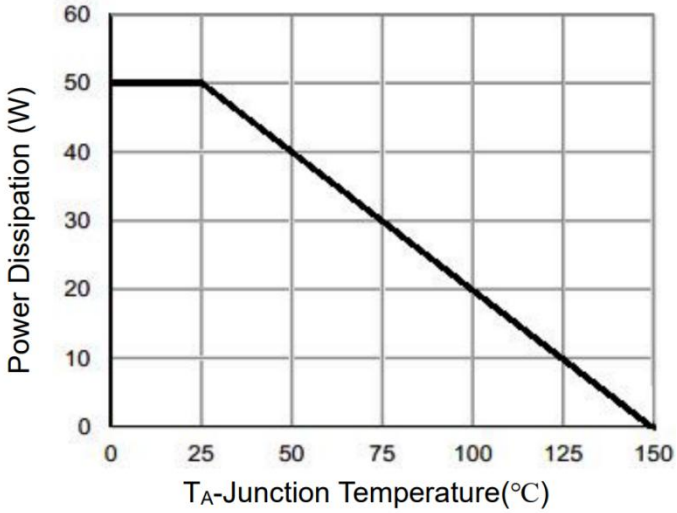


Figure 9 Power De-rating

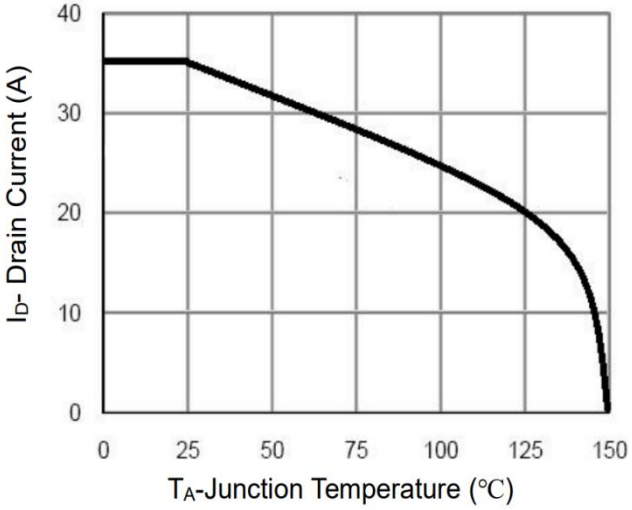


Figure 10 Current De-rating

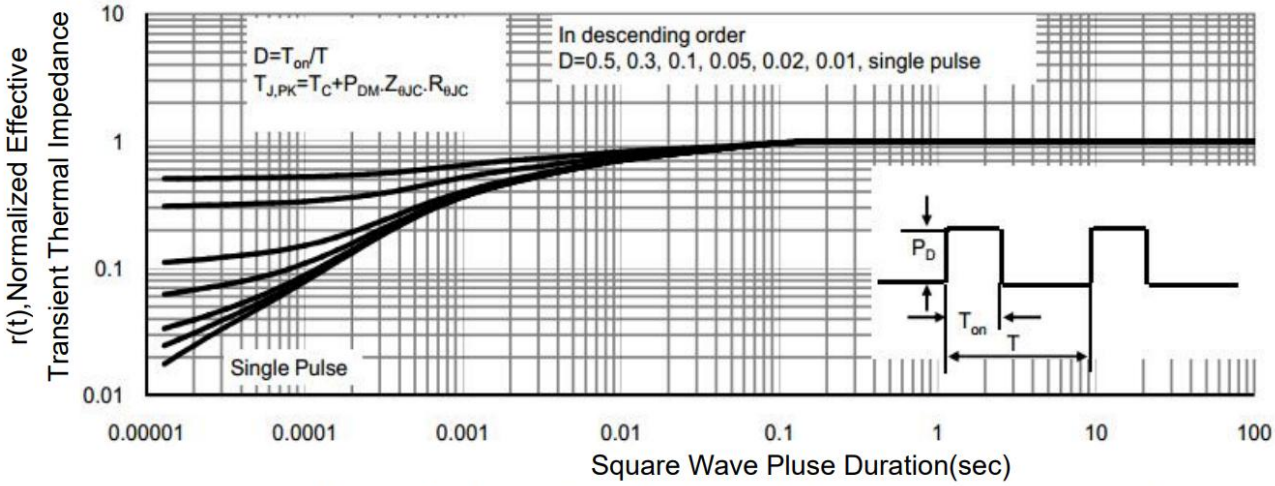
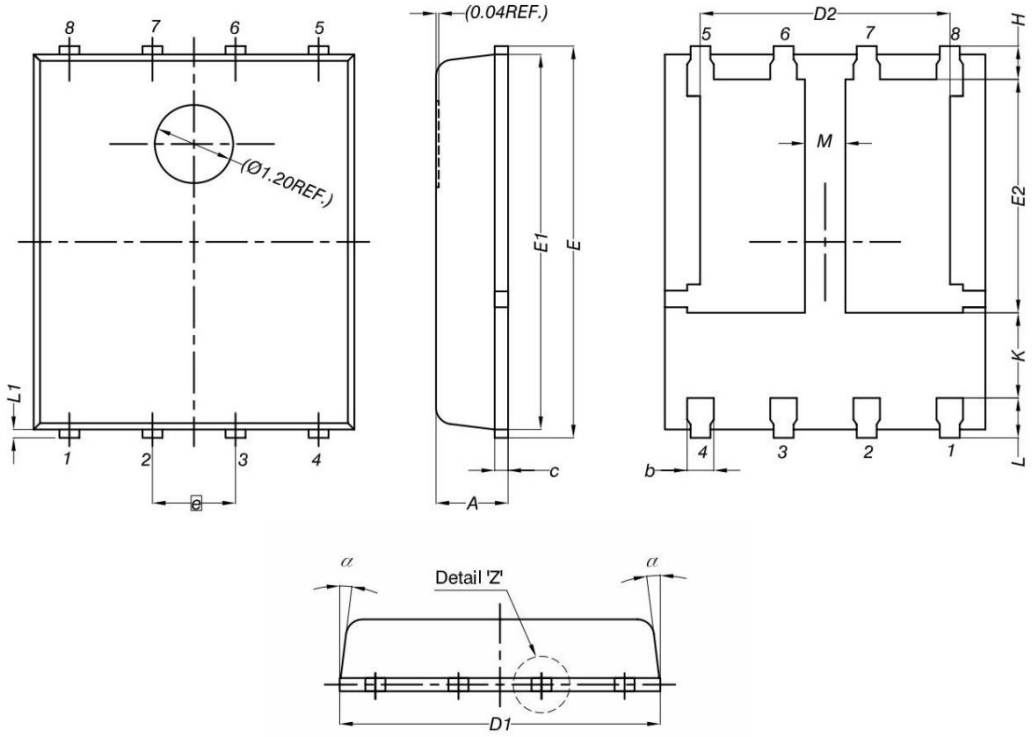


Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.90	1.10	0.035	0.043
b	0.33	0.51	0.013	0.020
C	0.20	0.30	0.008	0.012
D1	4.80	5.00	0.189	0.197
D2	3.61	3.96	0.142	0.156
E	5.90	6.10	0.232	0.240
E1	5.70	5.90	0.224	0.232
E2	3.37	3.78	0.133	0.149
e	1.27BSC.		1.27BSC.	
H	0.41	0.61	0.016	0.024
K	1.10	-	0.043	-
L	0.51	0.71	0.020	0.028
L1	0.06	0.20	0.002	0.008
M	0.50	-	0.020	-
a	0°	12°	0°	12°