

3.0A Surface Mount Schottky Barrier Rectifiers 20V-100V

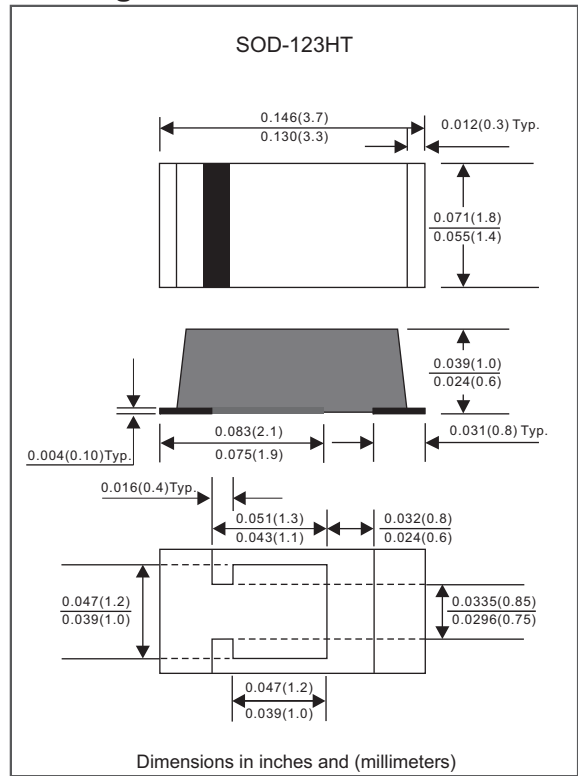
Features

- Well package design with solder pad on the bottom for best thermal performance
- Low profile surface mounted application in order to optimize board space
- Tiny plastic SMD package
- Low power loss, high efficiency
- High current capability, low forward voltage drop
- High surge capability
- Silicon epitaxial planar chip, metal silicon junction
- Lead-free parts meet RoHS requirements
- Compliant to Halogen-free
- Suffix "-Q1" for AEC-Q101

Mechanical data

- Epoxy : UL94-V0 rated flame retardant
- Case : Molded plastic, SOD-123HT
- Terminals :Plated terminals, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any

Package outline



Maximum ratings (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOLS	AS320-MHT-Q1	AS330-MHT-Q1	AS340-MHT-Q1	AS350-MHT-Q1	AS360-MHT-Q1	AS380-MHT-Q1	AS3100-MHT-Q1	UNITS
Maximum repetitive peak reverse voltage	V_{RRM}	20	30	40	50	60	80	100	V
Maximum RMS voltage	V_{RMS}	14	21	28	35	42	56	70	V
Maximum continuous reverse voltage	V_R	20	30	40	50	60	80	100	V
Maximum average forward rectified current	I_O	3.0							A
Non-repetitive peak forward surge current 8.3ms single half sine-wave	I_{FSM}	70							A
Typical diode junction capacitance (Note 1)	C_J	160							pF
Operating junction temperature range	T_J	-55 to +150							$^{\circ}\text{C}$
Storage temperature range	T_{STG}	-65 to +175							$^{\circ}\text{C}$

Electrical characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOLS	AS320-MHT-Q1	AS330-MHT-Q1	AS340-MHT-Q1	AS350-MHT-Q1	AS360-MHT-Q1	AS380-MHT-Q1	AS3100-MHT-Q1	UNITS	
Maximum instantaneous forward voltage at $I_F=3.0\text{A}$	V_F	0.55			0.70		0.85		V	
Maximum reverse leakage current at rated V_R	I_R	$T_J=25^{\circ}\text{C}$ 0.2				$T_J=100^{\circ}\text{C}$ 10				mA mA

Thermal characteristics

PARAMETER	SYMBOLS	AS320-MHT-Q1	AS330-MHT-Q1	AS340-MHT-Q1	AS350-MHT-Q1	AS360-MHT-Q1	AS380-MHT-Q1	AS3100-MHT-Q1	UNITS
Typical thermal resistance junction to ambient (Note 2)	$R_{\theta JA}$	70							$^{\circ}\text{C} / \text{W}$
Typical thermal resistance junction to case (Note 2)	$R_{\theta JC}$	35							$^{\circ}\text{C} / \text{W}$

Notes 1: $f=1\text{MHz}$ and applied 4V DC reverse voltage
 2: Mounted on FR-4 PCB Copper, minimum recommended pad layout

Rating and characteristic curves

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

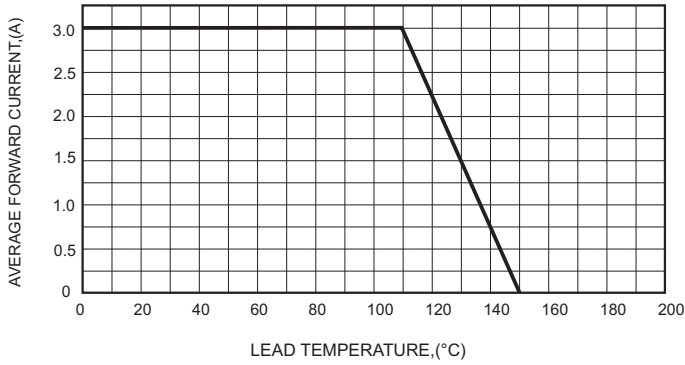


FIG.2-TYPICAL FORWARD CHARACTERISTICS

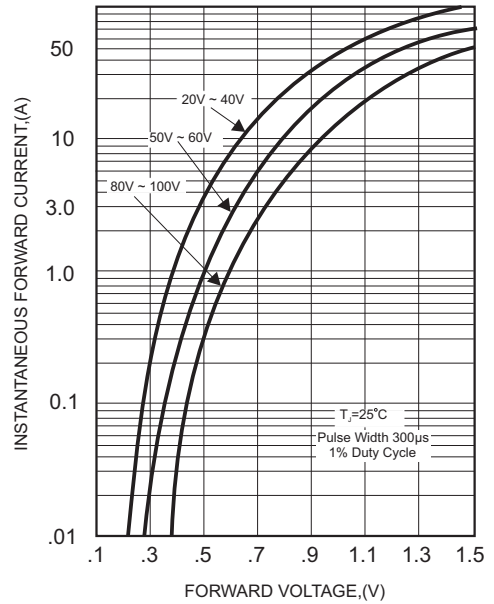


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

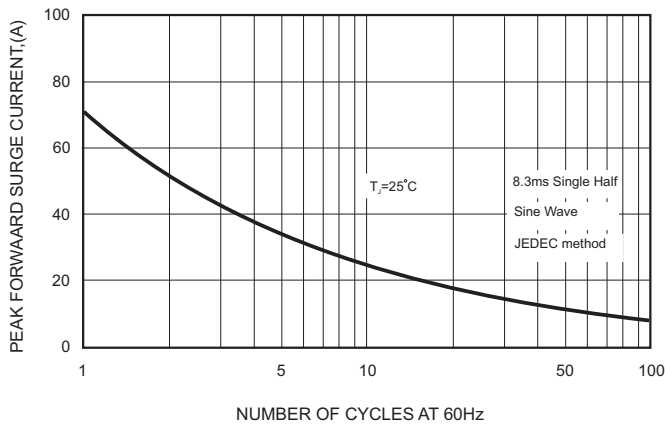


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

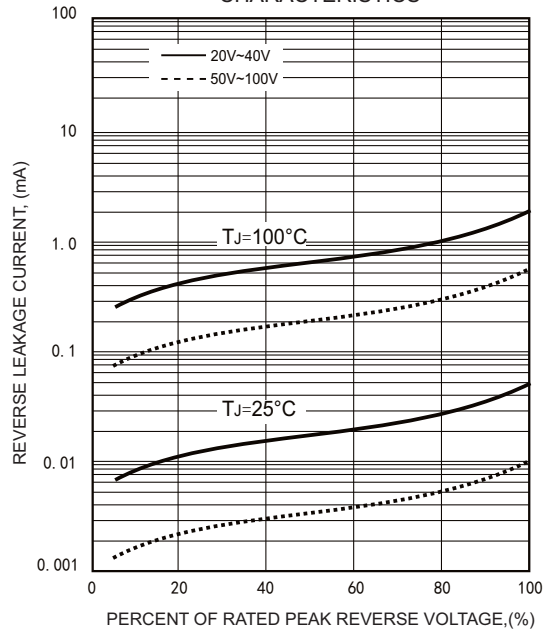
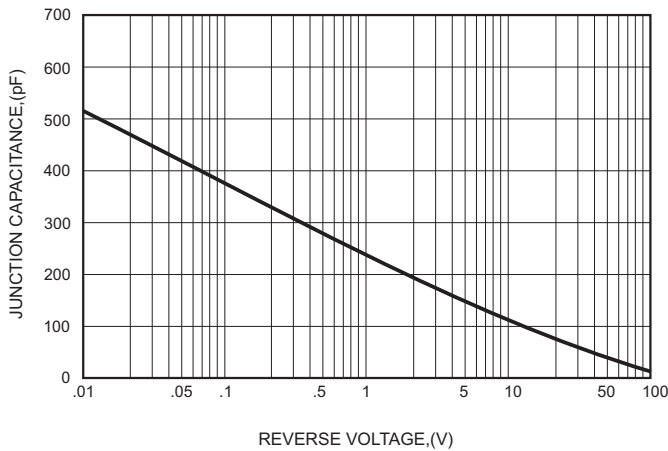




FIG.4-TYPICAL JUNCTION CAPACITANCE



Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
AS320-MHT-Q1	32
AS330-MHT-Q1	33
AS340-MHT-Q1	34
AS350-MHT-Q1	35
AS360-MHT-Q1	36
AS380-MHT-Q1	38
AS3100-MHT-Q1	310

Suggested solder pad layout

