

Features

- 2-pin lead-less package
- Junction capacitance (Max value: 26pF)
- Peak Pulse Current (8/20μs): 8A
- IEC61000-4-2 (ESD) ±20kV (air), ±15kV (contact)
- Low clamping voltage
- Low leakage current
- Working voltages:24V
- RoHS Compliant
- Compliant to Halogen-free

Applications

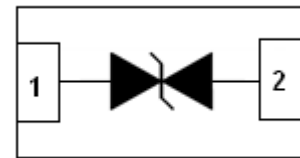
- DC port
- Notebooks and Handhelds, Audio Players,Peripherals
- Personal Digital Assistants, Portable Instrumentation
- Keypads, Side Keys, LCD Displays

Mechanical Data

- Package: DFN-2L
- Lead Finish:Matte Tin
- Case Material: "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Marking Information: See Below



DFN-2L(0402)



PIN Configuration



Absolute maximum ratings (T=25°C , RH=45%-75%, Unless otherwise specified)

Parameters	Symbol	Value	Unit
Peak Pulse Power (tp=8/20μs waveform)	P _{PP}	480	W
Peak Pulse Current (8/20μs)	I _{PP}	8	A
ESD follow IEC 61000-4-2 (Air) ESD follow IEC 61000-4-2 (Contact)	V _{ESD}	±20 ±15	KV
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Electrical Characteristics (T=25°C , RH=45%-75%, Unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				24	V
Reverse Breakdown Voltage	V_{BR}	$I_R = 1mA$	27		31	V
Reverse Leakage Current	I_R	$V_R = 24V$			0.2	μA
Clamping voltage	V_C	$I_{PP} = 1A, T_P=8/20\mu s$			40	V
Clamping voltage	V_C	$I_{PP} = 8A, T_P=8/20\mu s$			60	V
Junction capacitance	C_J	$V_R = 0V, f = 1MHz$		20	26	pF

Characteristics (Typical)

FIG1: Power rating derating curve

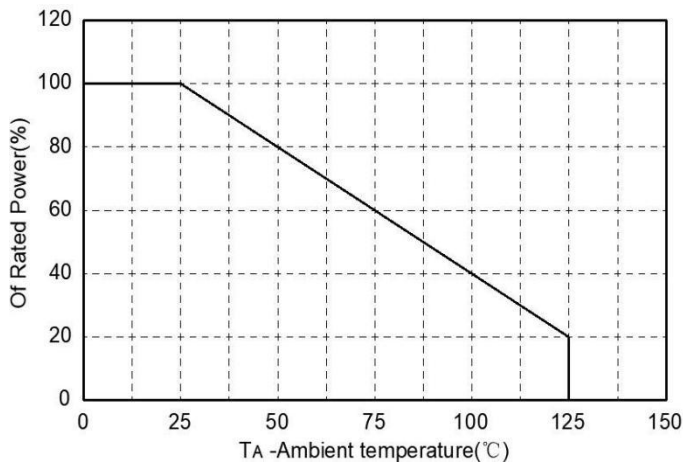


FIG2: pulse Waveform

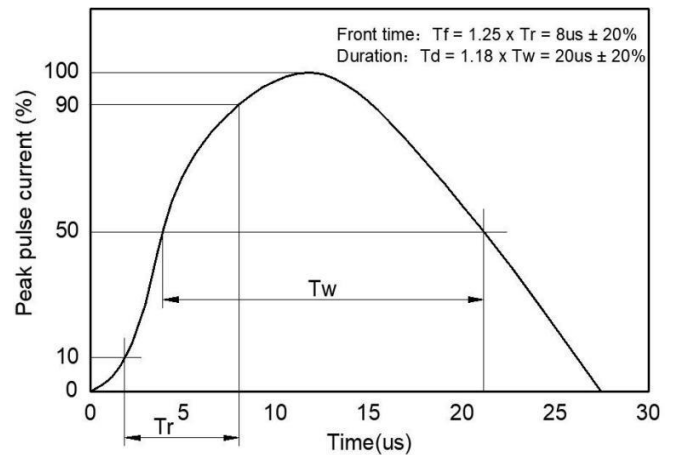


FIG3: Capacitance between terminals characteristics

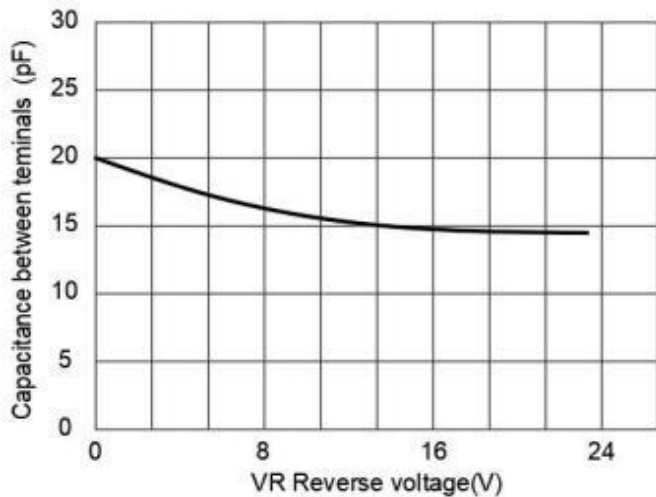
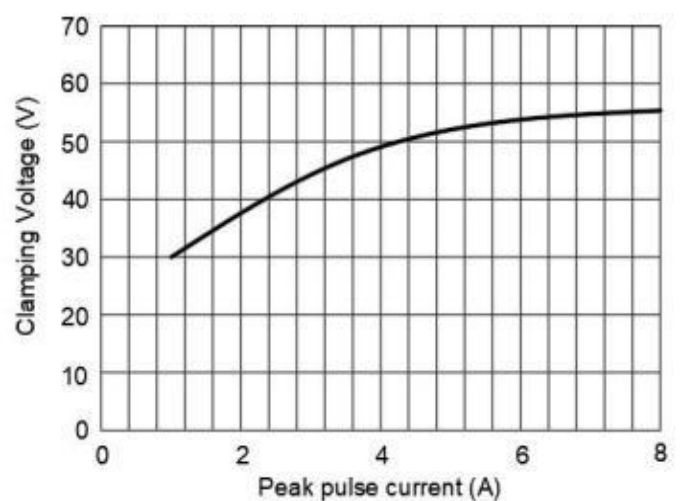
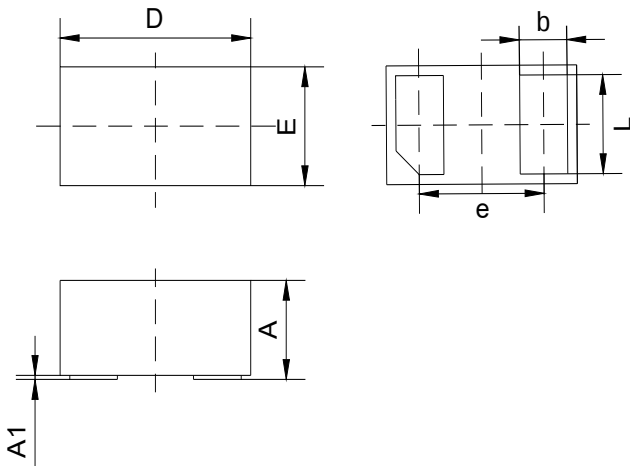


FIG4: Clamping Voltage vs. Peak Pulse Current

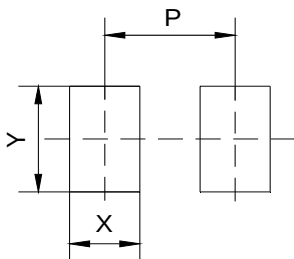


Outline Dimensions



Symbol	Dimension in Millimeters	
	min	max
A	0.41	0.51
A1	0	0.05
D	0.9	1.1
E	0.55	0.65
e	(0.65)	
b	0.2	0.3
L	0.34	0.55

Recommended PCB Layout



Symbol	Dimension in Millimeters	
	typ	
X	(0.5)	
Y	(0.7)	
P	(0.8)	

Notes:

This recommended land pattern is for reference purposes only.
 Please consult your manufacturing group to ensure your PCB design guidelines are met