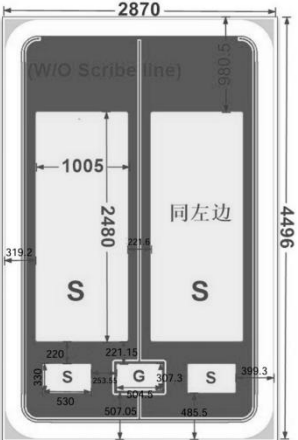
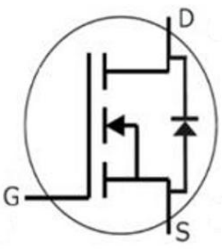


Physical Characteristics

	Die size: 2870 μm x 4496 μm (without scribe line) Gate pad: 504 μm x 307 μm Gross die / per 6" wafer = 1119 pcs	
	Main characteristics: $V_{DS} = 2200\text{V}$ $I_D(T_C=25^\circ\text{C}) = 48\text{A}$ $R_{DS(on)TYP} = 80\text{m}\Omega@18\text{V}$	

Mechanical Data

Parameter	Parameter
Nominal Back Metal Composition, Thickness	Ti- Ni - Ag
Nominal Front Metal Composition, Thickness	Al(4 μm)
Wafer Diameter	150mm
Wafer Thickness	175 $\mu\text{m} \pm 10\mu\text{m}$
Scribe line width	80 μm

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Condition	Value	Unit
Drain-Source Voltage	V_{DSmax}	$V_{GS} = 0\text{V}, I_D = 100\mu\text{A}$	2200	V
Gate-Source Voltage	V_{GSmax}	AC ($f > 1\text{Hz}$)	-10/+25	V
Gate-Source Voltage	V_{GSOP}	Static	-4/+18	V
Continuous Drain Current	I_D	$V_{GS}=18\text{V}, T_C=25^\circ\text{C}$	48	A
	I_D	$V_{GS}=18\text{V}, T_C=100^\circ\text{C}$	34	A
Pulsed Drain Current	$I_{D,pulse}$	Pulse with t_p limited by T_{jmax} at 1 ms	77	A
		Pulse with t_p limited by T_{jmax} at 100 μs	151	A
Junction Temperature	T_J		-55 ~ +175	$^\circ\text{C}$
Storage Temperature	T_{STG}		-55 ~ +175	$^\circ\text{C}$

Note1: Assumes a $R_{th(jc)}$ will be less than 0.44K/W.

Electrical characteristics (T_J=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 100μA	2200			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 2200V, V _{GS} = 0V		1	50	μA
Gate-Source leakage current	I _{GSS}	V _{GS} = 18V, V _{DS} = 0V			250	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 5mA		3.2		V
		V _{DS} = V _{GS} , I _D = 5mA, T _J = 175°C		2.3		
Drain-source on-resistance	R _{DS(on)}	V _{GS} = 18V, I _D = 20A		80		mΩ
		V _{GS} = 18V, I _D = 20A, T _J = 175°C		160		
Transconductance	g _{fs}	V _{DS} = 18V, I _D = 20A		9		S
		V _{DS} = 18V, I _D = 20A, T _J = 175°C		8		
Dynamic characteristics						
Input Capacitance	C _{iss}	V _{DS} = 1000V, V _{GS} = 0V, f = 100kHz V _{AC} = 25mV		2442		pF
Output Capacitance	C _{oss}			74		
Reverse Transfer Capacitance	C _{rss}			7		
Total Gate Charge	Q _g	V _{DS} = 1200V, V _{GS} = -4V/18V, I _D = 20A		81		nC
Gate-Source Charge	Q _{gs}			24		
Gate-Drain Charge	Q _{gd}			35.5		
Internal Gate Resistance	R _{G(int)}	f = 1 MHz, V _{AC} = 25mV		1.3		Ω
Source-Drain Diode characteristics						
Diode Forward Current	I _S	V _{GS} = -4V, T _C = 25°C		65		A
Diode Forward voltage	V _{SD}	V _{GS} = -4V, I _{SD} = 10A		3.8		V
		V _{GS} = -4V, I _{SD} = 10A, T _J = 175°C		3.3		
Diode pulse Current	I _{S,pulse}	V _{GS} = -4V, pulse width t _p limited by T _{Jmax}		77		A