

### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_D@25^{\circ}C$
650V	150mΩ@18V	37A

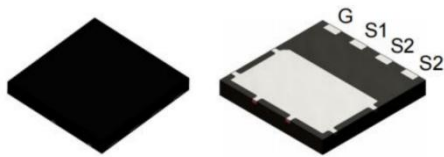
### Feature

- Wide bandgap SiC MOSFET technology
- Low On-Resistance with High Blocking Voltage
- Low Capacitances with High-Speed switching
- Low reverse recovery(Qrr)

### Application

- Switch Mode Power Supplies
- Renewable Energy
- On Board Charger
- High Voltage DC/DC Converters

### Package

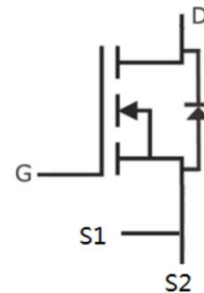


DFN8\*8

### Marking



### Circuit diagram



### Absolute maximum ratings ( $T_C=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Condition	Value	Unit
Drain-Source Voltage	$V_{DS}$	$V_{GS} = 0V, I_D = 100\mu A$	650	V
Gate-Source Voltage	$V_{GSmax}$	AC ( $f > 1 \text{ Hz}$ )	-10/+25	V
Gate-Source Voltage	$V_{GSOP}$	Static	-4/+18	V
Continuous Drain Current	$I_D$	$V_{GS} = 18V, T_C=25^{\circ}C$	37	A
	$I_D$	$V_{GS} = 18V, T_C=100^{\circ}C$	27	
Pulsed Drain Current	$I_{D,pulse}$	Pulse with $t_p$ limited by $T_{jmax}$ at 1ms Pulse with $t_p$ limited by $T_{jmax}$ at 100μs	56 141	A
Power Dissipation	$P_D$	$T_C=25^{\circ}C$	185	W
Thermal Resistance (Typ)	$R_{\theta JC}$	Junction-to-Case	0.81	$^{\circ}C/W$
Junction Temperature	$T_J$		-55~ +175	$^{\circ}C$
Storage Temperature	$T_{STG}$		-55~ +175	$^{\circ}C$

### Electrical characteristics (T<sub>j</sub>=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100μA	650			V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V			50	μA
Gate-Source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = 18V, V <sub>DS</sub> = 0V			250	nA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 5mA		2.7		V
		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 5mA, T <sub>j</sub> = 175°C		1.8		
Drain-source on-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 18V, I <sub>D</sub> = 10A		105	150	mΩ
		V <sub>GS</sub> = 20V, I <sub>D</sub> = 10A		94		
		V <sub>GS</sub> = 18V, I <sub>D</sub> = 10A, T <sub>j</sub> = 175°C		140		
		V <sub>GS</sub> = 20V, I <sub>D</sub> = 10A, T <sub>j</sub> = 175°C		130		
Transconductance	g <sub>fs</sub>	V <sub>GS</sub> = 18V, I <sub>D</sub> = 10A		13		S
		V <sub>GS</sub> = 18V, I <sub>D</sub> = 10A, T <sub>j</sub> = 175°C		9		
<b>Dynamic characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V, f = 1 MHz V <sub>AC</sub> = 25mV		767		pF
Output Capacitance	C <sub>oss</sub>			55		
Reverse Transfer Capacitance	C <sub>rss</sub>			7		
Internal Gate Resistance	R <sub>G(int)</sub>	f = 1 MHz, V <sub>AC</sub> = 25mV		2.8		Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 400V, I <sub>D</sub> = 10A V <sub>GS</sub> = -4/18V		35.8		nC
Gate-Source Charge	Q <sub>gs</sub>			8.3		
Gate-Drain Charge	Q <sub>gd</sub>			12.1		
<b>Source-Drain Diode characteristics</b>						
Diode Forward Current	I <sub>S</sub>	V <sub>GS</sub> = -5V, T <sub>C</sub> = 25°C		35		A
Diode Forward voltage	V <sub>SD</sub>	V <sub>GS</sub> = -5V, I <sub>SD</sub> = 7.5A		4		V
		V <sub>GS</sub> = -5V, I <sub>SD</sub> = 7.5A, T <sub>J</sub> = 175°C		3.5		V
Diode pulse Current	I <sub>S, pulse</sub>	V <sub>GS</sub> = -5V, pulse width t <sub>p</sub> limited by T <sub>jmax</sub>		56		A

## Typical Characteristics

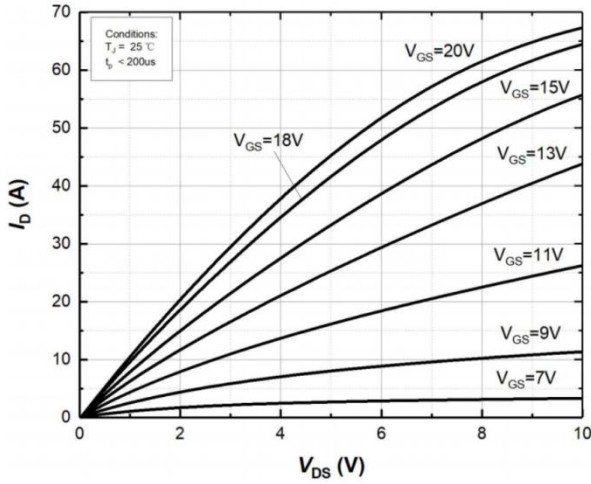


Figure 1. Output characteristics at  $T_j=25^\circ\text{C}$

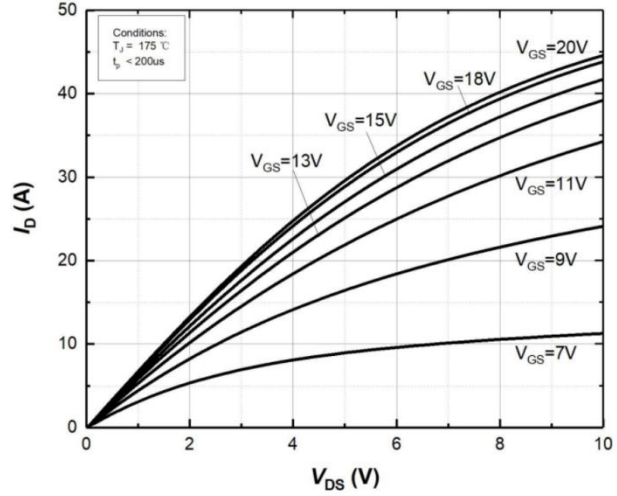


Figure 2. Output characteristics at  $T_j=175^\circ\text{C}$

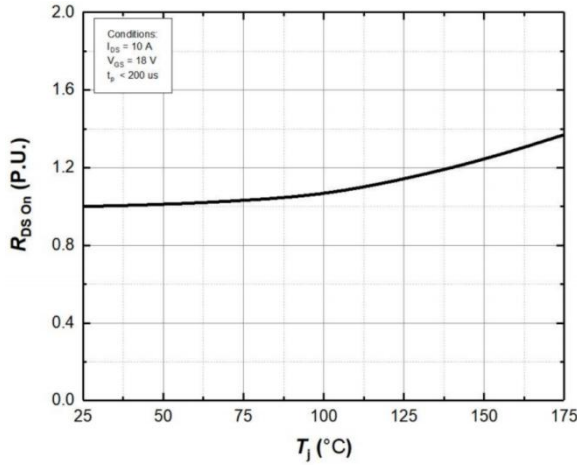


Figure 3. Normalized On-Resistance vs. Temperature

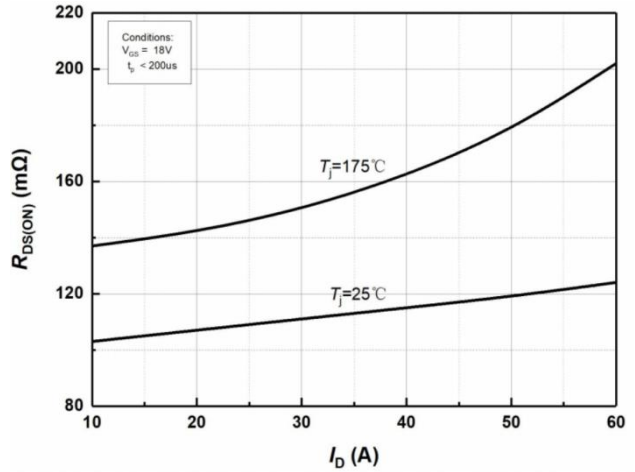


Figure 4. On-Resistance vs. Drain current for Various Temperature

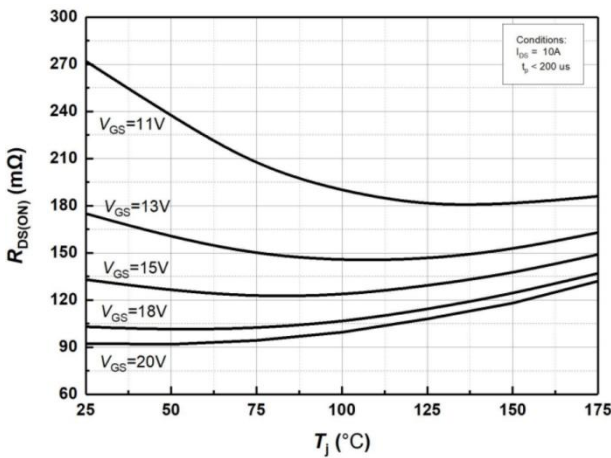


Figure 5. On-Resistance vs. Temperature for Various Gate Voltage

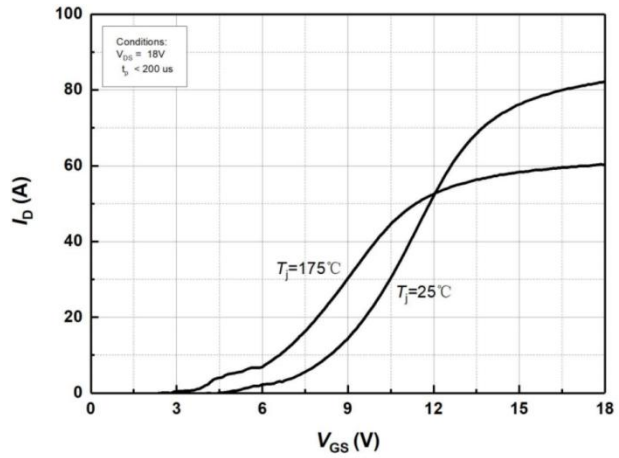


Figure 6. Transfer Characteristics for Various Junction Temperatures

## Typical Characteristics

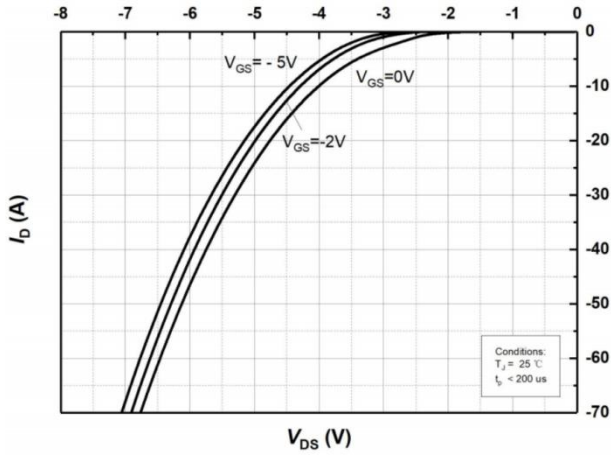


Figure 7. Body Diode Characteristics at  $T_j=25^\circ\text{C}$

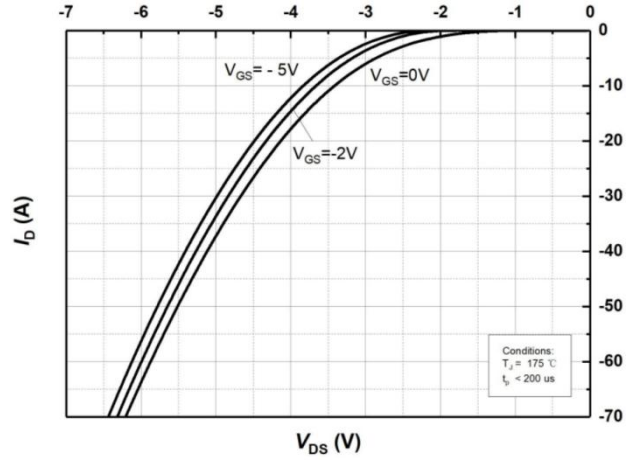


Figure 8. Body Diode Characteristics at  $T_j=175^\circ\text{C}$

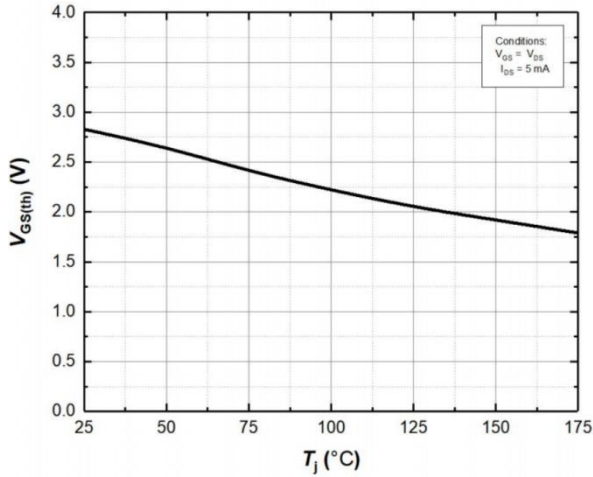


Figure 9. Threshold Voltage vs. Temperature

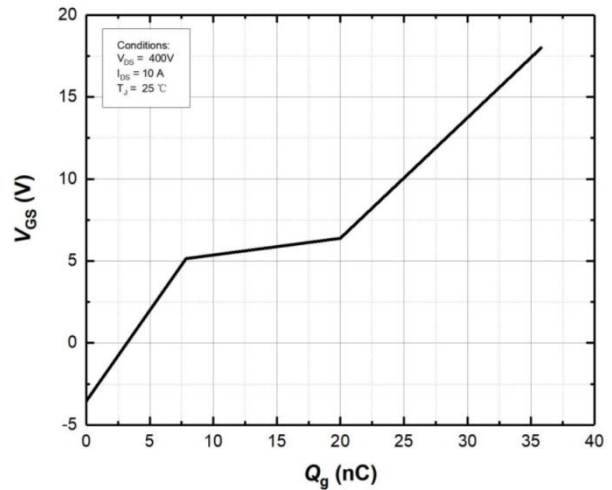


Figure 10. Gate Charge Characteristics

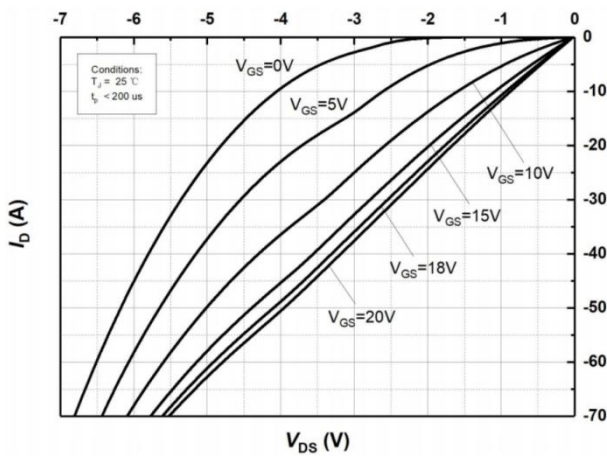


Figure 11. 3rd Quadrant Characteristic at  $T_j=25^\circ\text{C}$

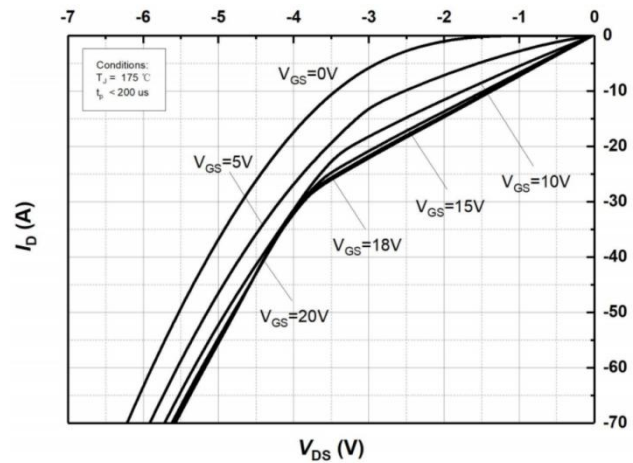


Figure 12. 3rd Quadrant Characteristic at  $T_j=175^\circ\text{C}$



## Typical Characteristics

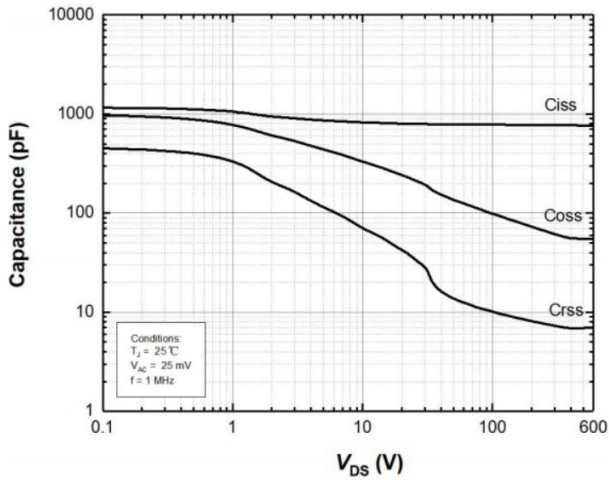


Figure 13. Capacitances vs. Drain-Source Voltage (0 – 600V)

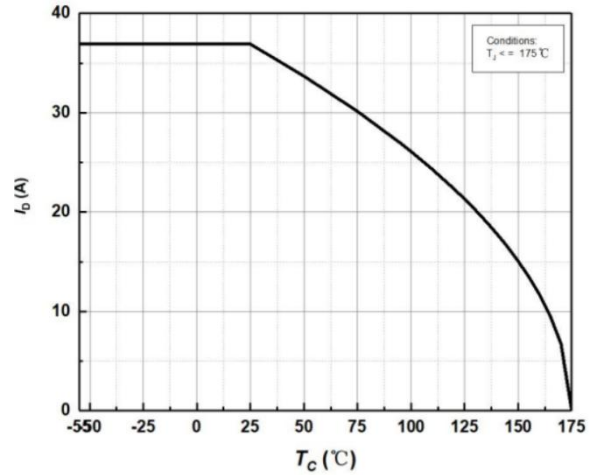


Figure 14. Continuous Drain Current Derating vs Case Temperature

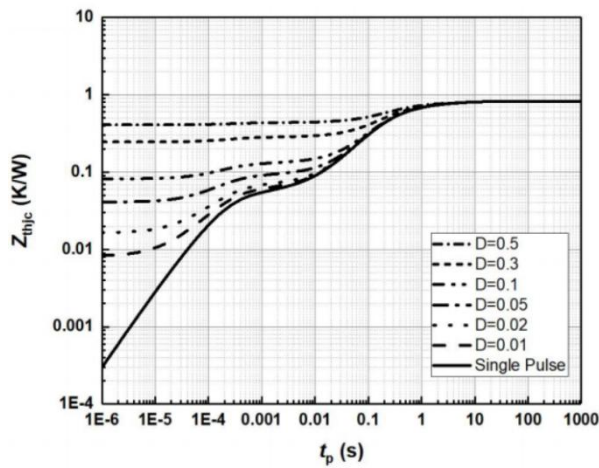


Figure 15. Transient Thermal Impedance (Junction – Case)

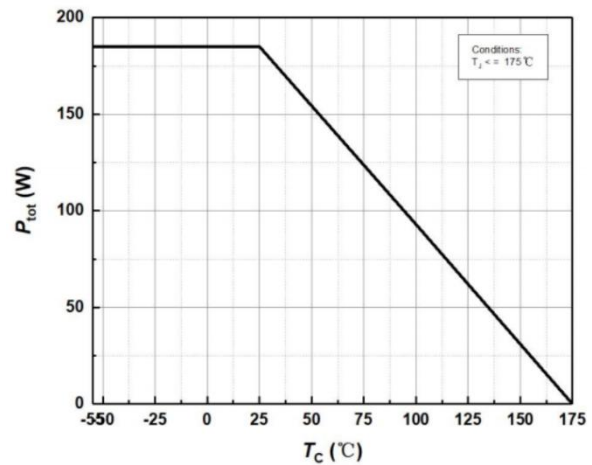


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

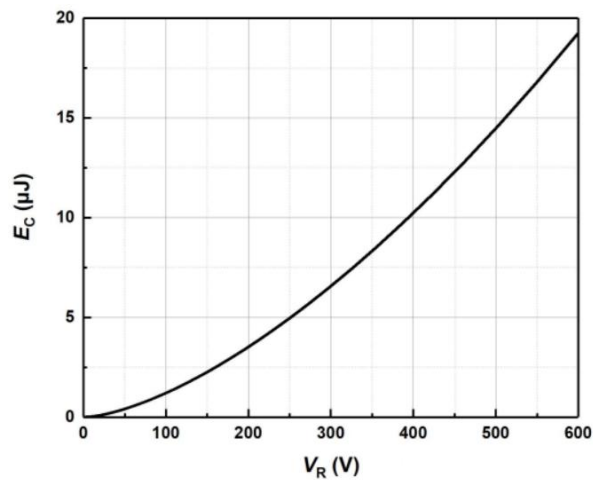


Figure 17. Output Capacitor Stored Energy

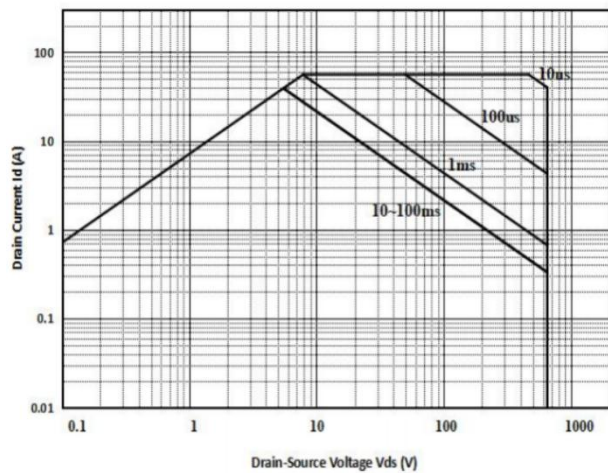
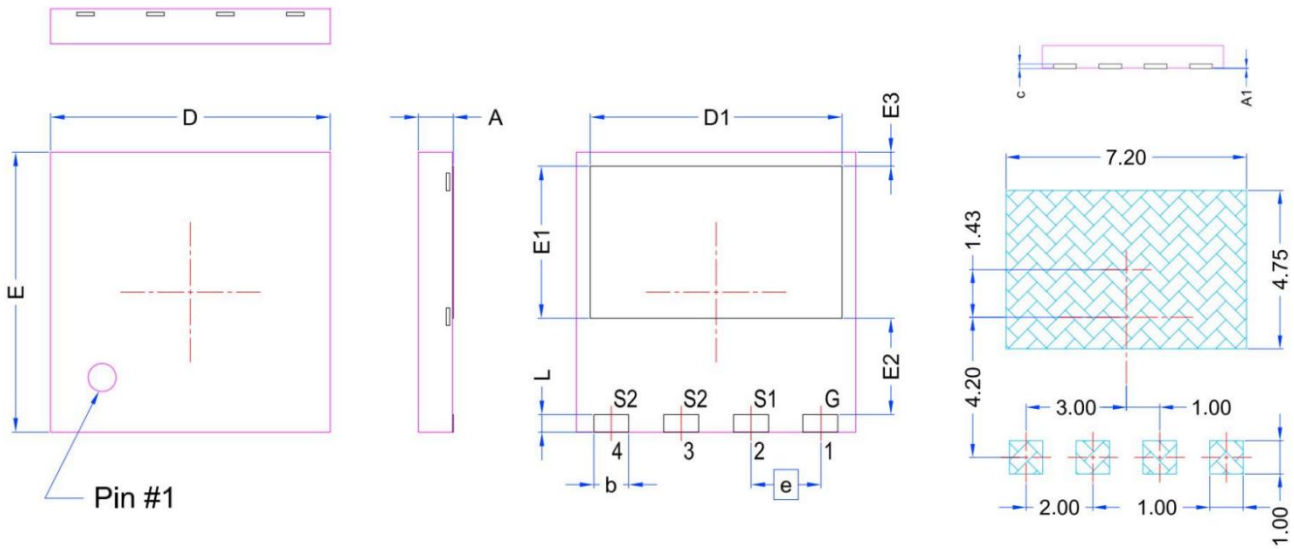


Figure 18. Safe Operating Area

### DFN8\*8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.000	0.050	0.000	0.002
b	0.900	1.100	0.035	0.043
c	0.100	0.300	0.004	0.012
D	7.900	8.100	0.311	0.319
D1	7.100	7.300	0.280	0.287
E	7.900	8.100	0.311	0.319
E1	4.250	4.450	0.167	0.175
E2	2.650	2.850	0.104	0.112
E3	0.300	0.500	0.012	0.020
e	2.000 BSC		0.079	0.000
L	0.400	0.600	0.016	0.024