

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
-60V	35mΩ@-10V	-30A
	38mΩ@-4.5V	

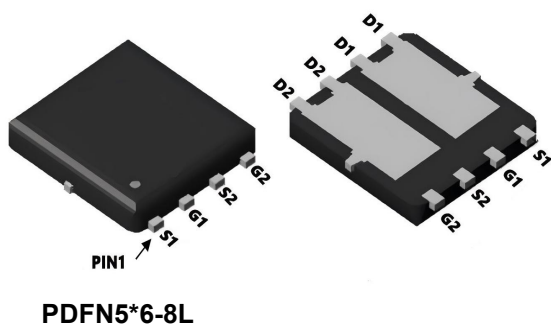
Feature

- Advanced trench technology
- Excellent $R_{DS(on)}$
- Low gate charge

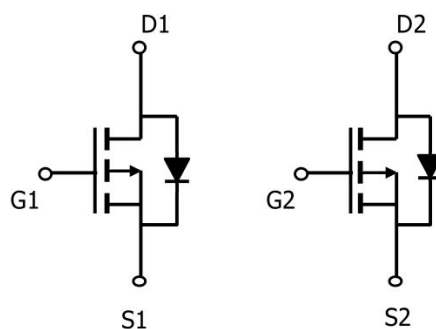
Application

- Lithium battery protection
- Wireless impact
- Mobile phone fast charging

Package



Circuit diagram



Marking



Absolute maximum ratings (T_c=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current ¹⁾ (V _{GS} = -10V)	I _D	-30	A
Continuous Drain Current ¹⁾ (V _{GS} = -10V, T _c = 100°C)	I _D (100°C)	-27	A
Pulsed Drain Current ²⁾	I _{DM}	-85	A
Single Pulse Avalanche Energy ³⁾	E _{AS}	113	mJ
Power Dissipation ⁴⁾	P _D	52.1	W
Thermal Resistance Junction to Case ¹⁾	R _{θJC}	2.4	°C/W
Operating Junction Temperature	T _J	-55 ~ +150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Electrical characteristics (T_c=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-60			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =-48V, V _{GS} =0V, T _J =25°C			-1	μA
Gate-body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1	-1.6	-2.5	V
Drain-source on-resistance ²⁾	R _{DS(on)}	V _{GS} =-10V, I _D =-10A		28	35	mΩ
		V _{GS} =-4.5V, I _D =-8A		33	38	
Dynamic characteristics ⁶⁾						
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, f =1MHz		3635		pF
Output Capacitance	C _{oss}			224		
Reverse Transfer Capacitance	C _{rss}			141		
Total Gate Charge	Q _g	V _{DS} =-20V, V _{GS} =-4.5V I _D =-12A		25		nC
Gate-Source Charge	Q _{gs}			6.7		
Gate-Drain Charge	Q _{gd}			5.5		
Turn-on delay time	t _{d(on)}	V _{DS} =-15V, V _{GS} =-10V I _D =-1A, R _G =3.3Ω		38		nS
Turn-on rise time	t _r			23.6		
Turn-off delay time	t _{d(off)}			100		
Turn-off fall time	t _f			6.8		
Source-Drain Diode characteristics						
Diode Forward Current ^{1,5)}	I _S	V _D =V _G =0V , Force Current			-30	A
Diode Forward voltage ²⁾	V _{SD}	V _{GS} =0V, I _S =-1A, T _J =25°C			-1	V

Notes:

- 1) The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2) The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%.
- 3) The EAS data shows Max. rating. The test condition is V_{DD} = -48V, V_{GS} = -10V, L = 0.1mH, I_{AS} = -47.6A.
- 4) The power dissipation is limited by 150°C junction temperature.
- 5) The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.
- 6) Guaranteed by design, not subject to production.

Typical Characteristics

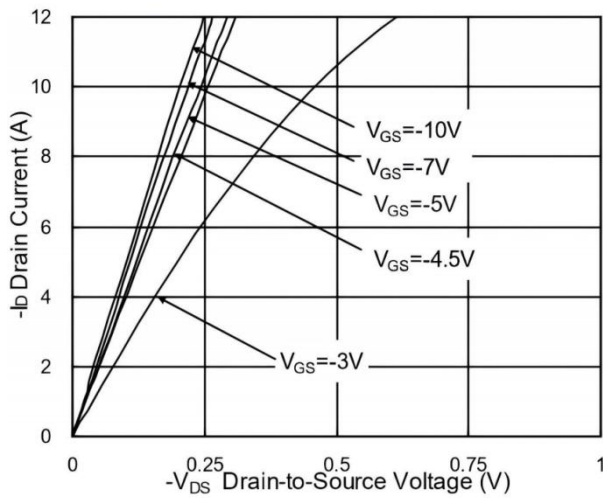


Fig.1 Typical Output Characteristics

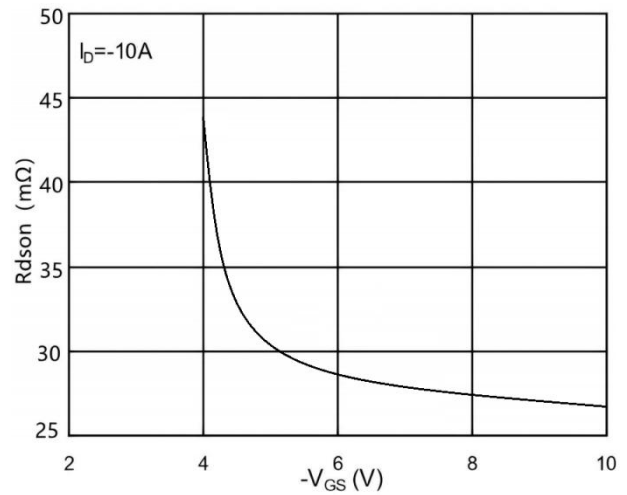


Fig.2 On-Resistance v.s Gate-Source

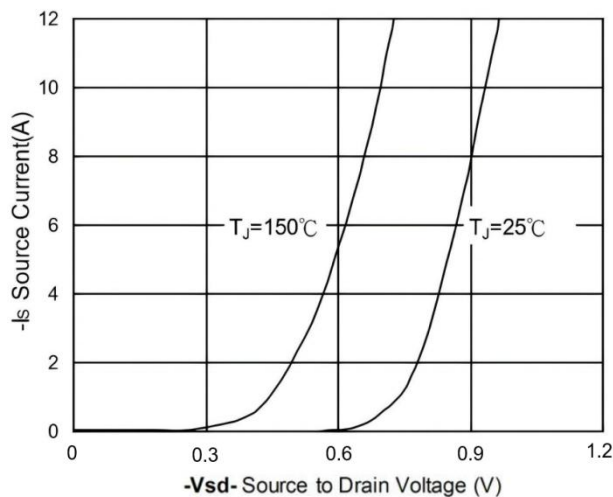


Fig.3 Forward Characteristics Of Reverse

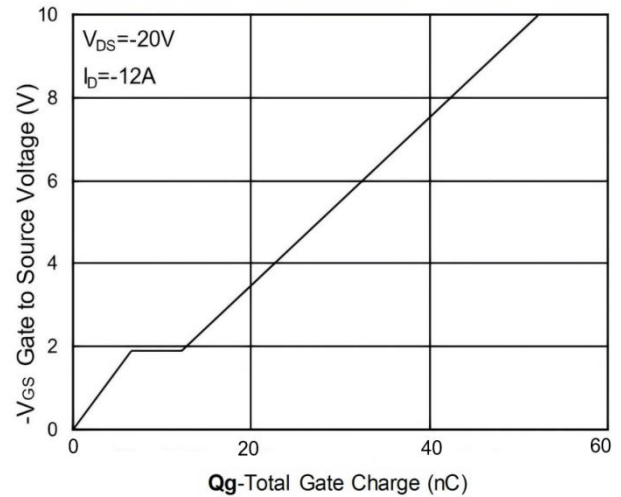


Fig.4 Gate-Charge Characteristics

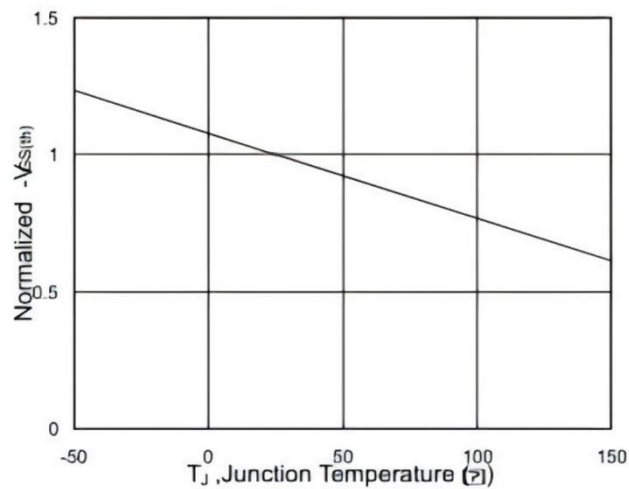


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

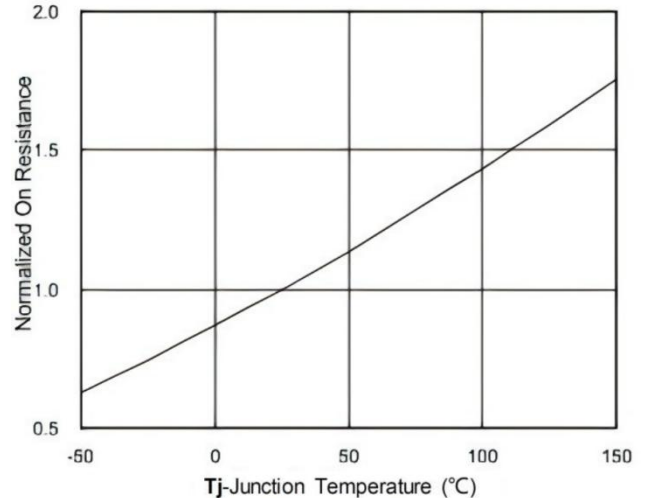


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

Typical Characteristics

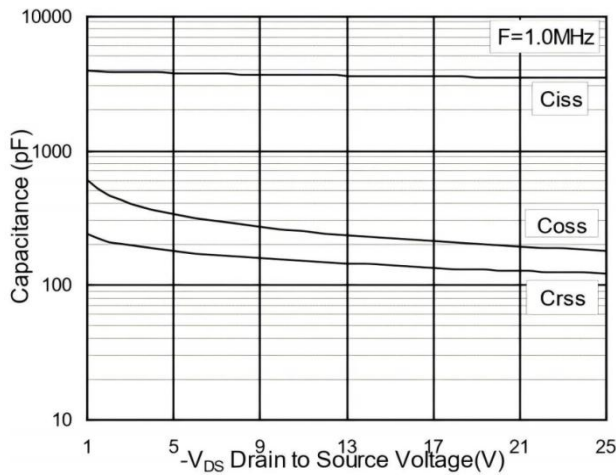


Fig.7 Capacitance

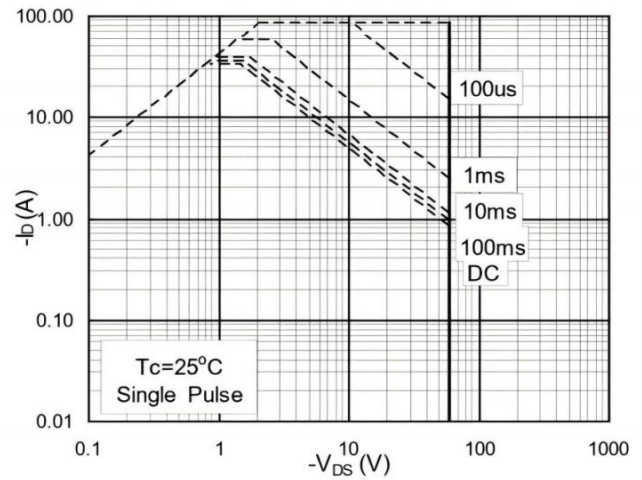


Fig.8 Safe Operating Area

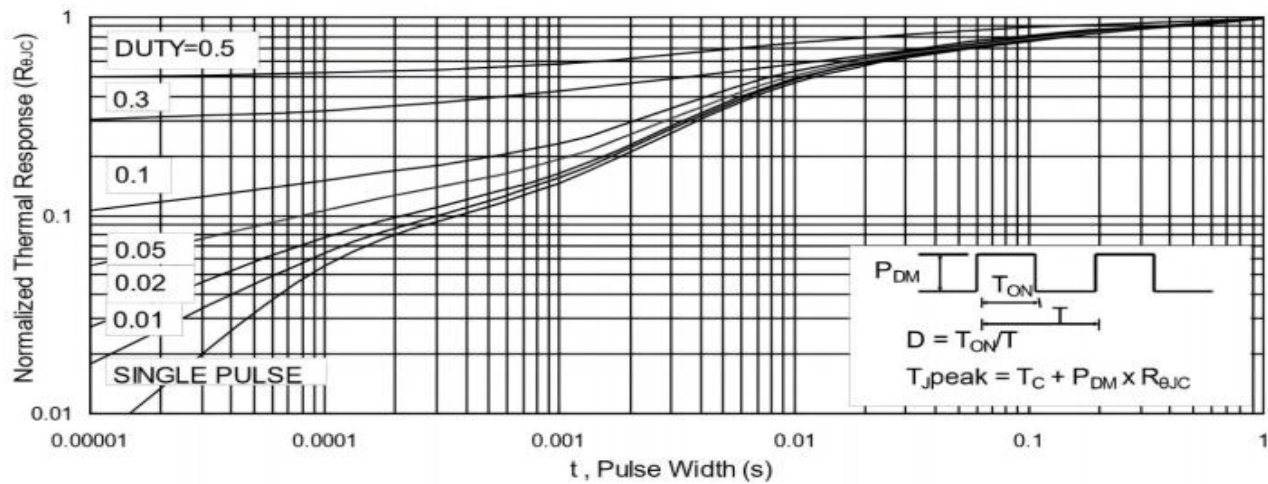


Fig.9 Normalized Maximum Transient Thermal Impedance

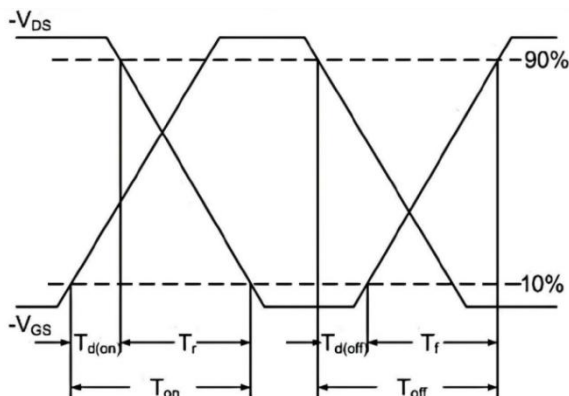


Fig.10 Switching Time Waveform

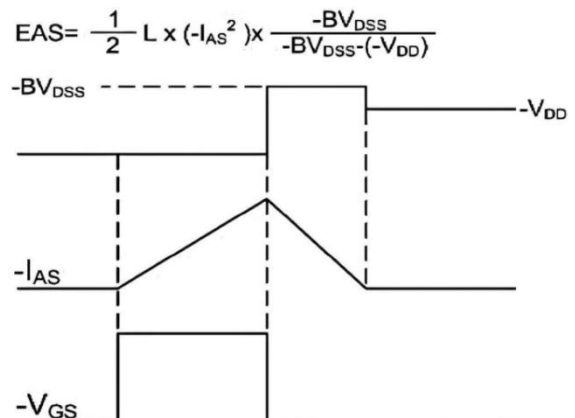
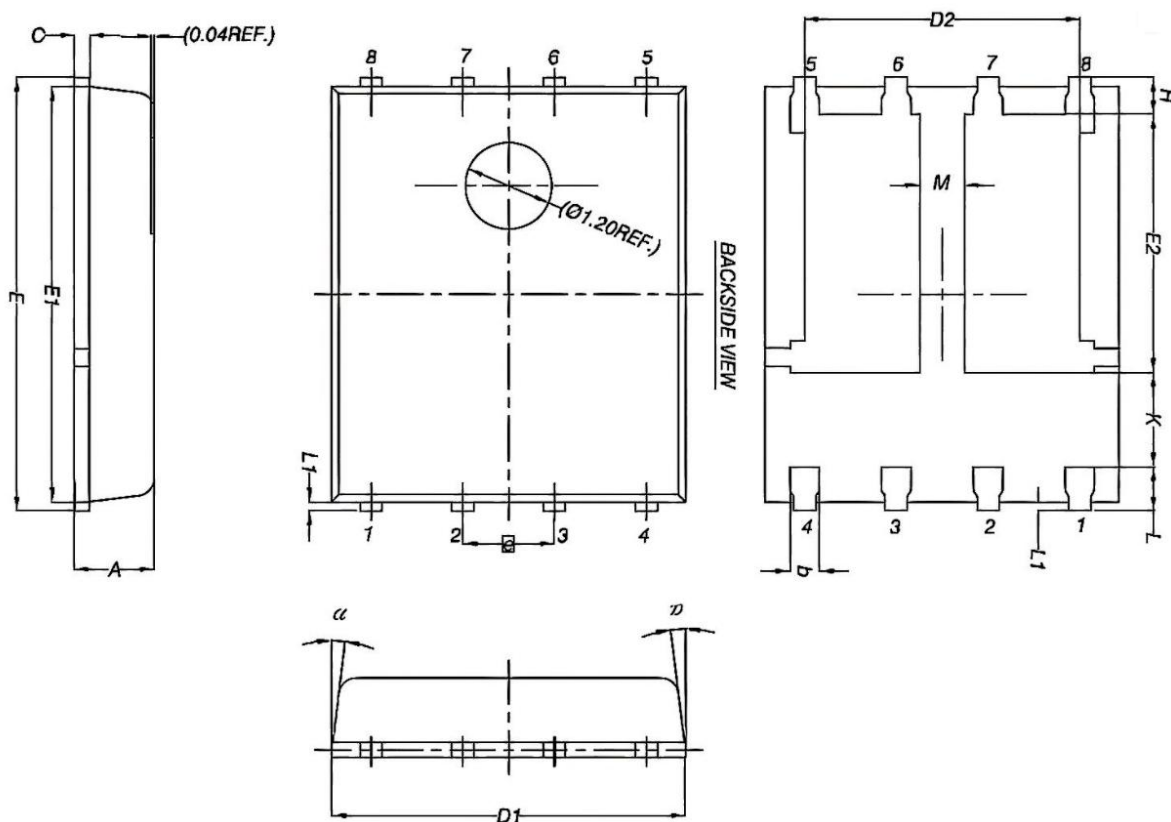


Fig.11 Unclamped Inductive Waveform

PDFN5*6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
b	0.330	0.510	0.013	0.020
C	0.200	0.300	0.008	0.012
D1	4.800	5.000	0.189	0.197
D2	3.610	3.960	0.142	0.156
E	5.900	6.100	0.232	0.240
E1	5.700	3.450	0.224	0.136
E2	3.380	3.200	0.133	0.126
e	1.270 BSC.		0.050 BSC.	
H	0.410	0.610	0.016	0.024
K	1.100	-	0.043	-
L	0.510	0.710	0.020	0.028
L1	0.060	0.200	0.002	0.008
M	0.500	-	0.020	-
a	0°	12°	10°	12°