

### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_D$
60V	16mΩ@10V	50A
	20mΩ@4.5V	

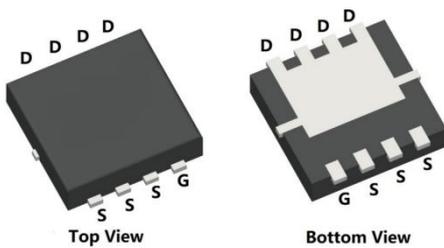
### Feature

- Advanced trench technology
- Excellent  $R_{DS(ON)}$
- Low gate charge

### Application

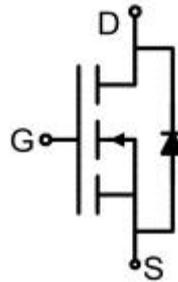
- Brushless motor
- Load switch
- Uninterruptible power supply

### Package

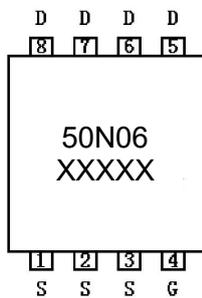


PDFN3.3\*3.3-8L

### Circuit diagram



### Marking



### Absolute maximum ratings (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current <sup>1)</sup> (V <sub>GS</sub> =10V)	I <sub>D</sub>	50	A
Continuous Drain Current <sup>1)</sup> (V <sub>GS</sub> =10V, T <sub>c</sub> =100°C)	I <sub>D</sub> (100 °C)	30	A
Pulsed Drain Current <sup>2)</sup>	I <sub>DM</sub>	90	A
Power Dissipation <sup>4)</sup>	P <sub>D</sub>	45	W
Single Pulse avalanche Energy <sup>3)</sup>	E <sub>AS</sub>	39.2	mJ
Thermal Resistance, Junction-to-Case <sup>1)</sup>	R <sub>θJC</sub>	2.8	°C/W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

### Electrical characteristics (T<sub>J</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	60			V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V			1	μA
Gate-body leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.2	1.8	2.5	V
Drain-source on-resistance <sup>2)</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		11	16	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		16	20	
<b>Dynamic characteristics<sup>6)</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1MHz		2423		pF
Output Capacitance	C <sub>oss</sub>			145		
Reverse Transfer Capacitance	C <sub>rss</sub>			97		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 4.5V I <sub>D</sub> = 15A		19.3		nC
Gate-Source Charge	Q <sub>gs</sub>			7.1		
Gate-Drain Charge	Q <sub>gd</sub>			7.6		
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V R <sub>G</sub> = 3.3Ω, I <sub>D</sub> = 15A		7.2		nS
Turn-on rise time	t <sub>r</sub>			50		
Turn-off delay time	t <sub>d(off)</sub>			36.4		
Turn-off fall time	t <sub>f</sub>			7.6		
<b>Source-Drain Diode characteristics</b>						
Diode Forward Current <sup>1,5)</sup>	I <sub>S</sub>	V <sub>D</sub> = V <sub>G</sub> = 0V, Force Current			35	A
Diode Forward voltage <sup>2)</sup>	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A			1.0	V
Reverse Recovery Time	T <sub>rr</sub>	I <sub>F</sub> = 15A, di/dt = 100A/μs		16.3		nS
Reverse Recovery Charge	Q <sub>rr</sub>				11	

Notes:

- 1) The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2) The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
- 3) The EAS data shows Max. rating. The test condition is V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=38A.
- 4) The power dissipation is limited by 150°C junction temperature.
- 5) The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.
- 6) Guaranteed by design, not subject to production.

## Typical Characteristics

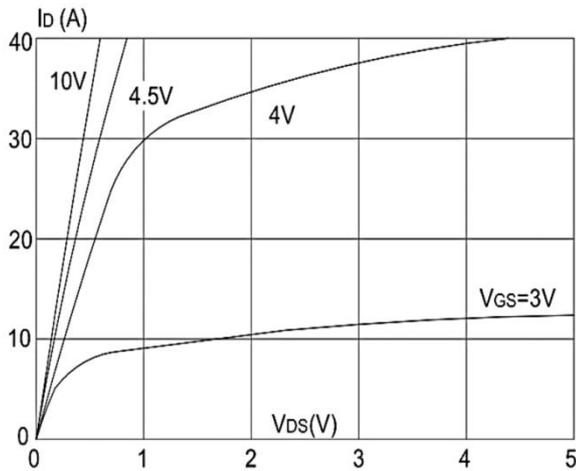


Figure 1: Output Characteristics

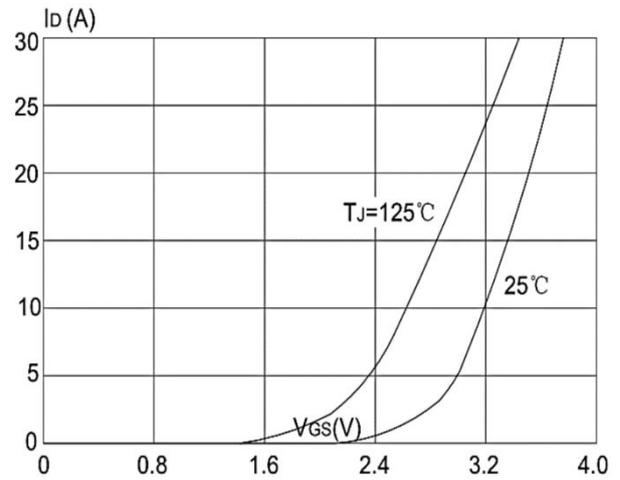


Figure 2: Typical Transfer Characteristics

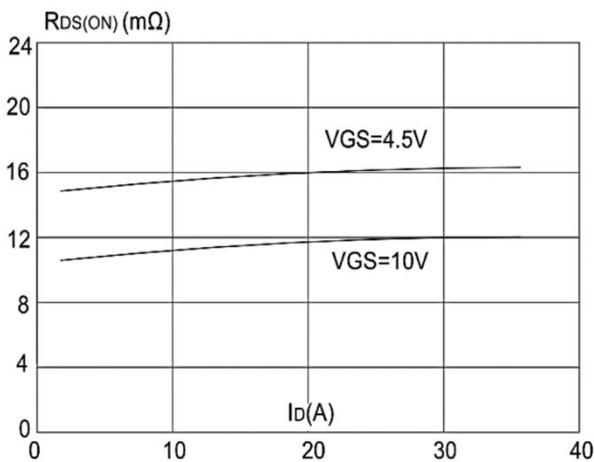


Figure 3: On-resistance vs. Drain Current

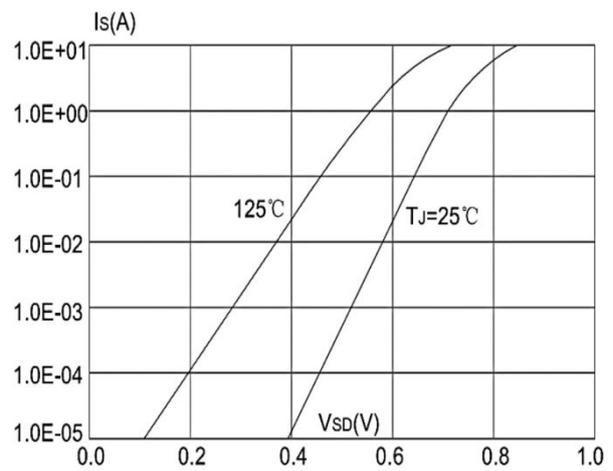


Figure 4: Body Diode Characteristics

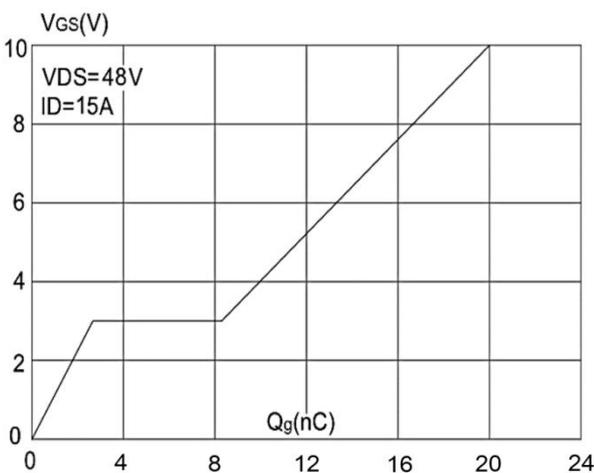


Figure 5: Gate Charge Characteristics

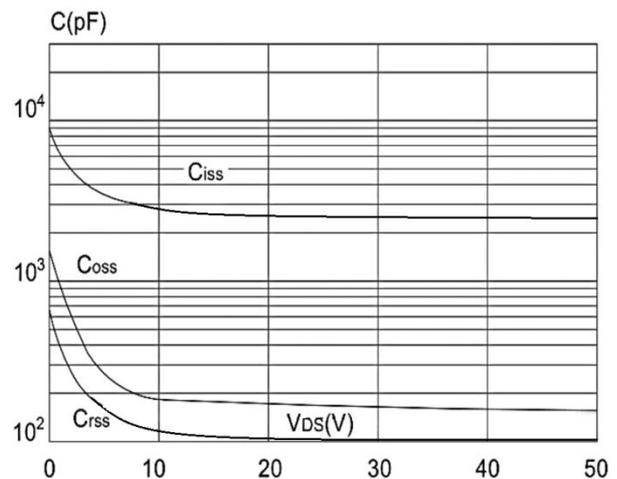
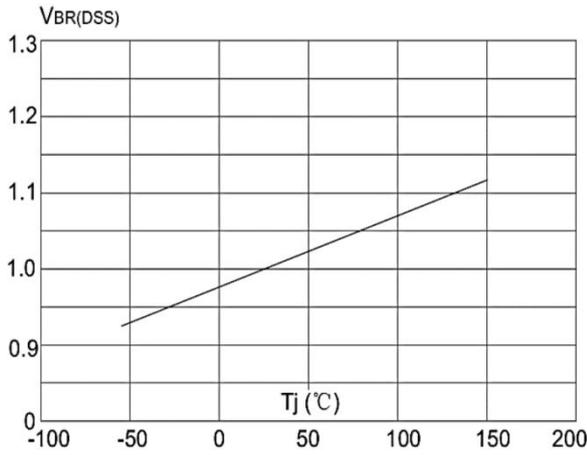
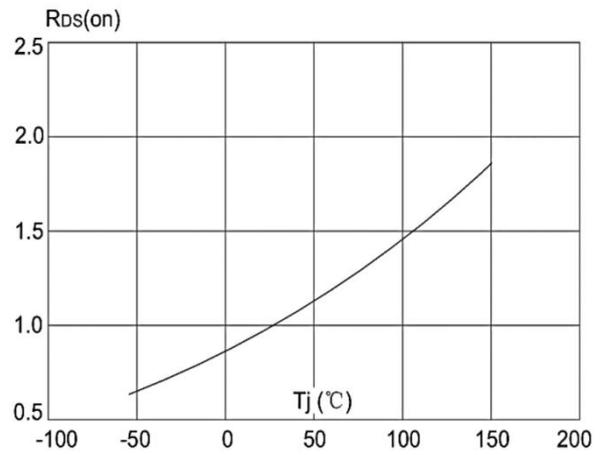


Figure 6: Capacitance Characteristics

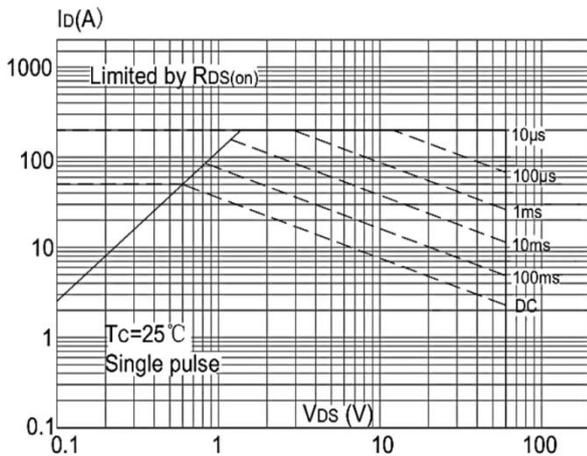
## Typical Characteristics



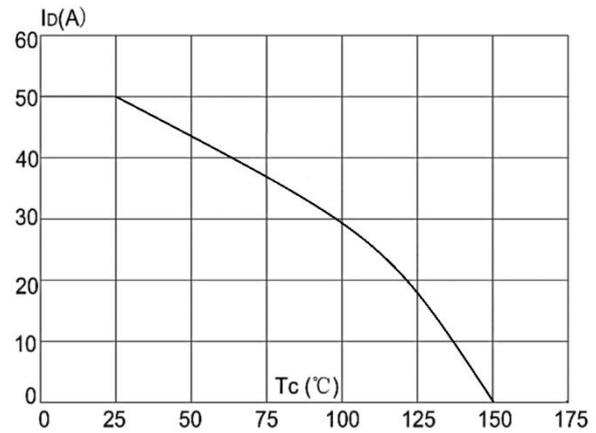
**Figure 7: Normalized Breakdown Voltage vs Junction Temperature**



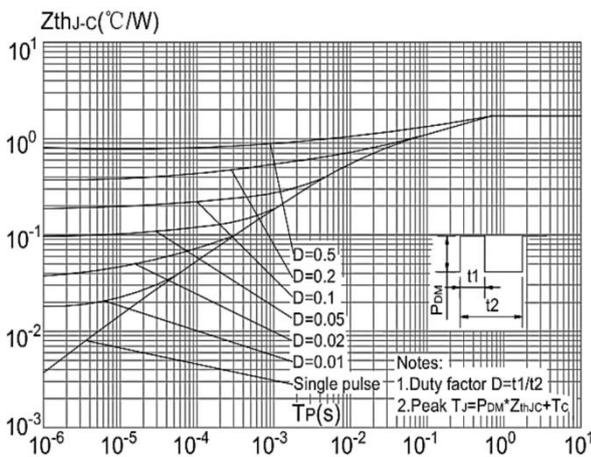
**Figure 8: Normalized on Resistance vs. Junction Temperature**



**Figure 9: Maximum Safe Operating Area**

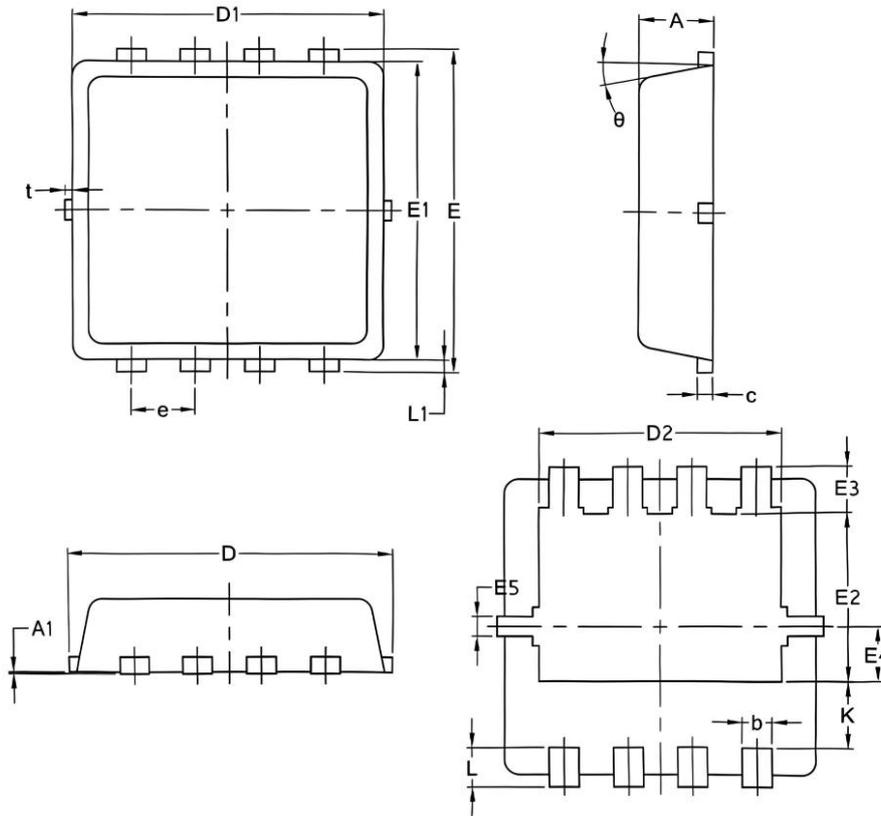


**Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature**



**Figure.11: Maximum Effective Transient Thermal**

### PDFN3.3\*3.3-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.850	0.028	0.033
A1	-	0.050	-	0.002
b	0.200	0.400	0.008	0.016
c	0.100	0.250	0.004	0.010
D	3.150	3.450	0.124	0.136
D1	3.000	3.250	0.118	0.128
D2	2.290	2.650	0.090	0.104
E	3.150	3.450	0.124	0.136
E1	2.900	3.200	0.114	0.126
E2	1.540	1.940	0.061	0.076
E3	0.280	0.650	0.011	0.026
E4	0.370	0.770	0.015	0.030
E5	0.100	0.300	0.004	0.012
e	0.600	0.700	0.024	0.028
K	0.590	0.890	0.023	0.035
L	0.300	0.500	0.012	0.020
L1	0.060	0.200	0.002	0.008
t	0.000	0.130	0.000	0.005
φ	10.000	14.000	0.394	0.551