

Product Summary

V _{(BR)DSS}	R _{D(on)MAX}	I _D
30V	1.2Ω@4.5V	0.4A
	1.6Ω@2.5V	
	2Ω@1.8V	

Feature

- Advanced trench cell design
- ESD Protected
- Suffix “-Q1” for AEC-Q101

Application

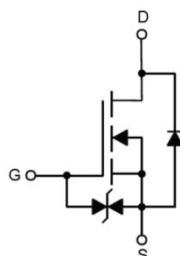
- Load switch appliances

Package

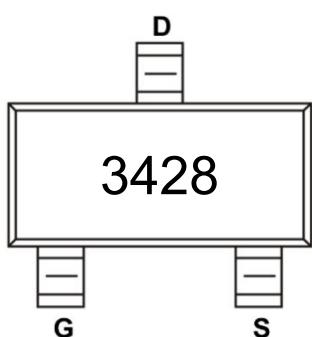


SOT-23

Circuit diagram



Marking



Absolute maximum ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±10	V
Continuous Drain Current ¹⁾	I _D	0.4	A
Continuous Drain Current ¹⁾ (T _A =70°C)	I _D	0.32	A
Pulsed Drain Current (t _p =10μs)	I _{DM}	1.6	A
Power Dissipation ¹⁾	P _D	0.42	W
Thermal Resistance Junction to Ambient ¹⁾	R _{θJA}	300	°C/W
Operating Junction Temperature	T _J	-55 ~ +150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Electrical characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	30			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =24V, V _{GS} =0V			1	μA
Gate-body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±8V			±10	μA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.4		1	V
Drain-source on-resistance ²⁾	R _{DS(on)}	V _{GS} =4.5V, I _D =0.3A			1.2	Ω
		V _{GS} =2.5V, I _D =0.2A			1.6	
		V _{GS} =1.8V, I _D =0.1A			2	
		V _{GS} =1.5V, I _D =0.05A			3	
		V _{GS} =1.2V, I _D =0.02A			4	
Dynamic characteristics³⁾						
Input Capacitance	C _{iss}	V _{DS} =10V, V _{GS} =0V, f =1MHz		45		pF
Output Capacitance	C _{oss}			14		
Reverse Transfer Capacitance	C _{rss}			0.8		
Total Gate Charge	Q _g	V _{DS} =10V, V _{GS} =4.5V, I _D =0.3A		0.9		nC
Gate-Source Charge	Q _{gs}			0.3		
Gate-Drain Charge	Q _{gd}			0.2		
Turn-on delay time	t _{d(on)}	V _{DS} =10V, V _{GS} =4V, I _D =0.3A R _G =10Ω		8.3		nS
Turn-on rise time	t _r			5.7		
Turn-off delay time	t _{d(off)}			35		
Turn-off fall time	t _f			12		
Source-Drain Diode characteristics						
Diode Forward Current	I _s				0.4	A
Diode Forward voltage ²⁾	V _{SD}	V _{GS} =0V, I _s =0.3A			1.3	V

Notes:

1) The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2) The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%.

3) Guaranteed by design, not subject to production testing.

Typical Characteristics

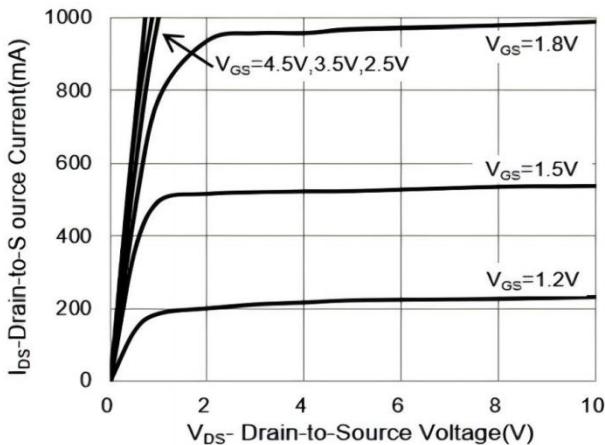


Fig 1 On-Region Characteristics

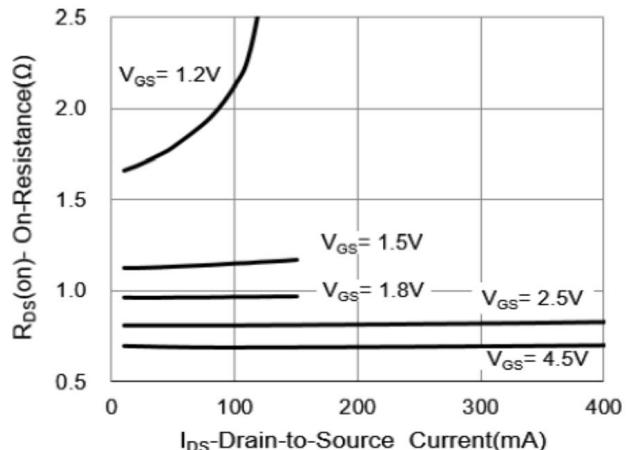


Fig 2 On-Resistance vs. Drain Current and Gate Voltage

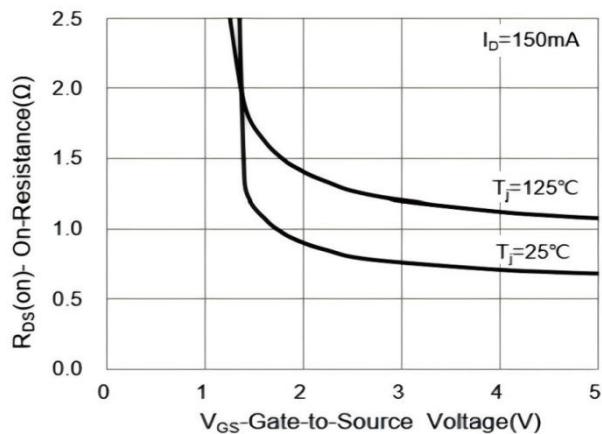


Fig 3 On-Resistance vs. Gate-Source Voltage

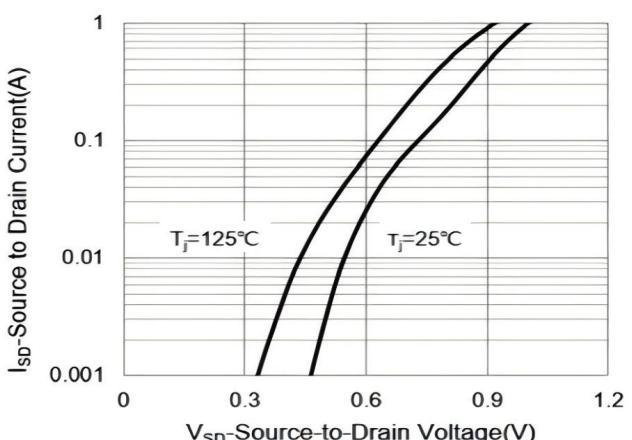


Fig 4 Body-Diode Characteristics

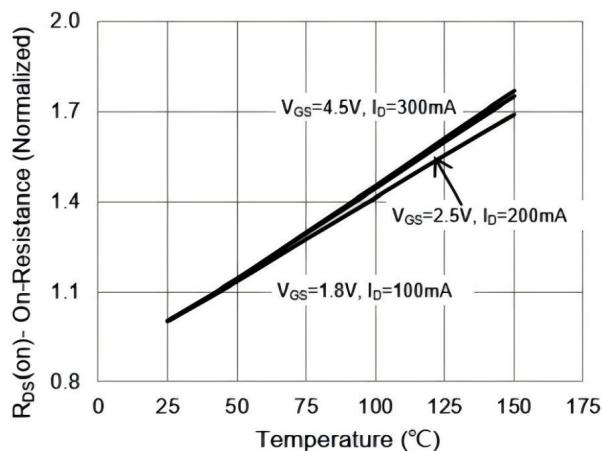


Fig 5 On-Resistance vs. Junction Temperature

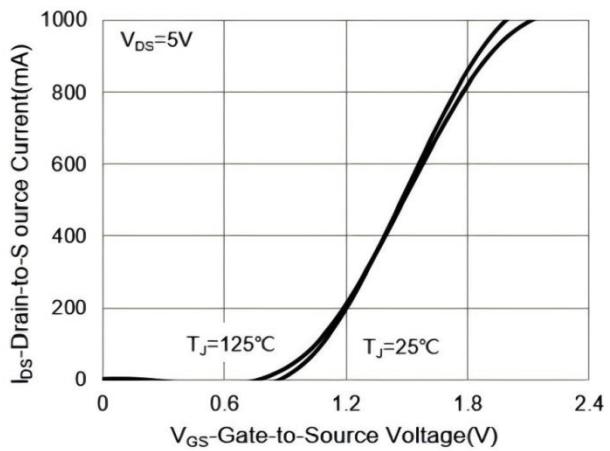


Fig 6 Transfer Characteristics

Typical Characteristics

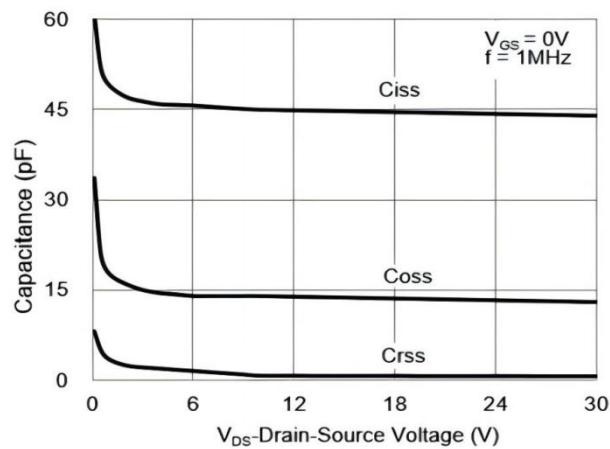


Fig 7 Capacitance Characteristics

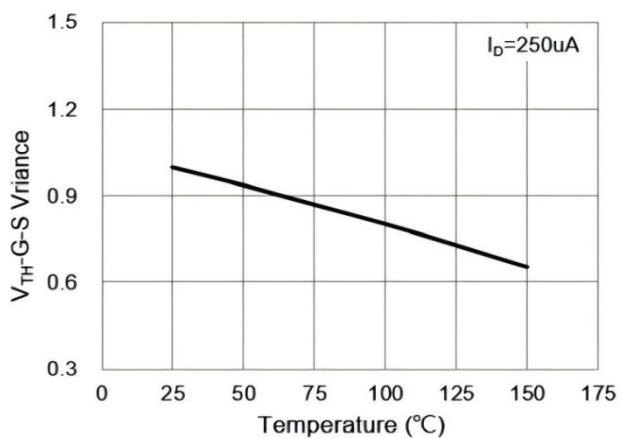
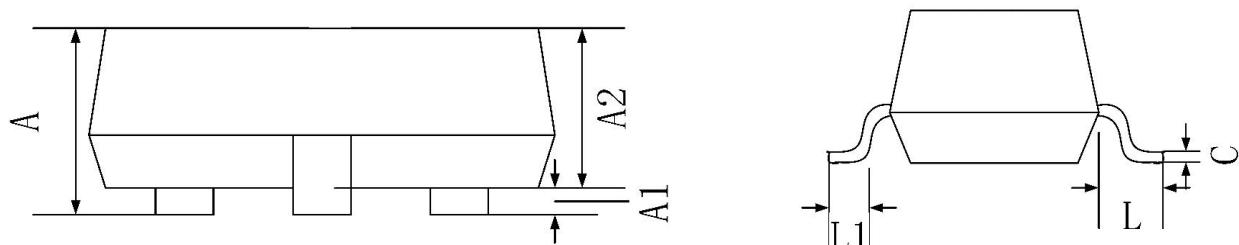
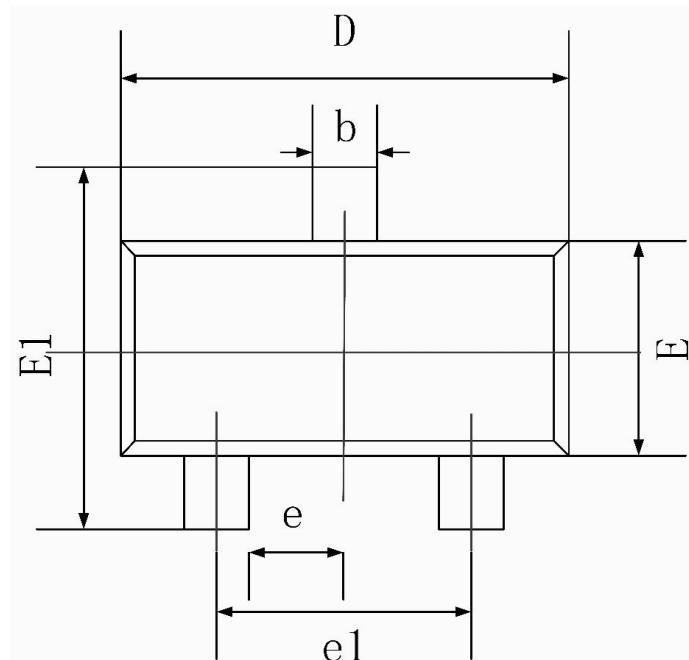


Fig 8 Gate Voltage vs. Junction Temperature

SOT-23 Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.100	0.035	0.043
b	0.300	0.500	0.012	0.020
C	0.050	0.200	0.002	0.008
D	2.700	3.100	0.106	0.122
E	1.100	1.500	0.043	0.059
E1	2.200	2.600	0.086	0.12
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020