

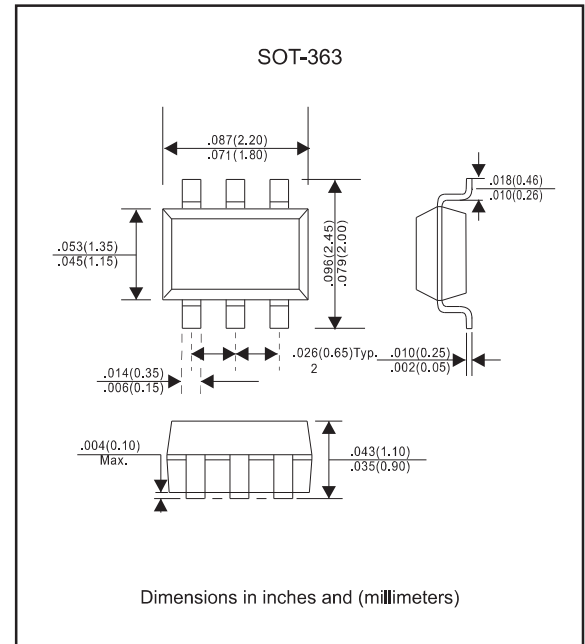
Features

- Low Forward Voltage Drop
- Fast Switching Time
- Surface Mount Package Ideally
- Compliant to Halogen-free
- Suffix “-Q1” for AEC-Q101

Mechanical data

- Case : SOT-363
- Mounting Position : Any

Package outline



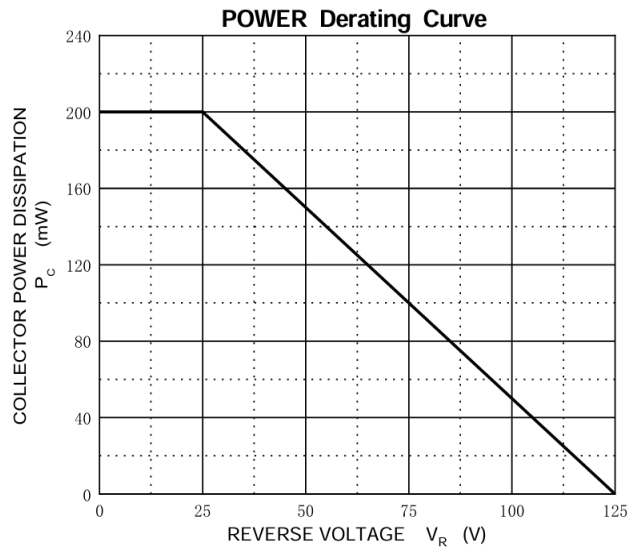
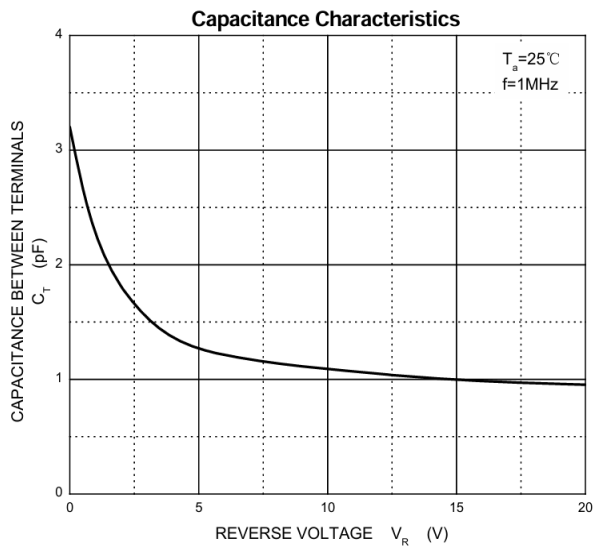
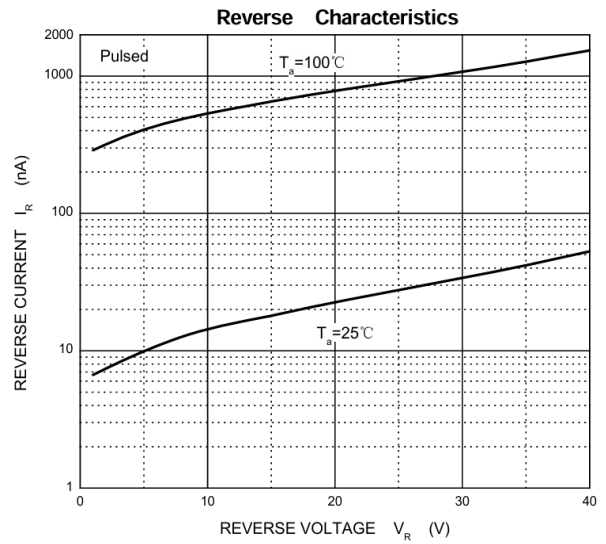
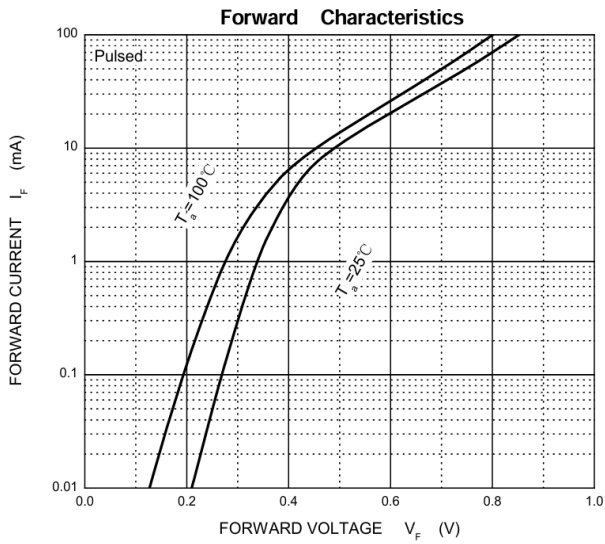
Absolute Maximum ratings (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbols	Value	Units
Non-Repetitive Peak reverse voltage	VRRM	40	V
DC Blocking Voltage	VR	40	V
Average Rectified Output Current	IO	200	mA
Power Dissipation	PD	200	mW
Typical thermal resistance	R θ JA	500	$^{\circ}\text{C}/\text{W}$
Junction Temperature	Tj	-40-+125	$^{\circ}\text{C}$
Storage temperature range	TSTG	-50-+150	$^{\circ}\text{C}$

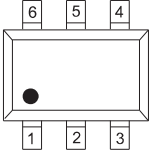
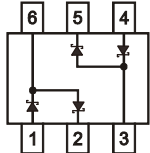
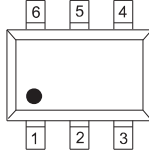
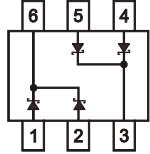
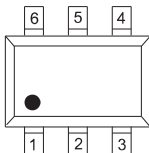
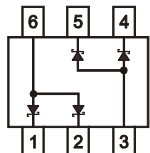
Electrical Characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Maximum reverse breakdown voltage	VR	IR=10uA	40			V
Maximum forward voltage	VF1	IF =1mA			0.38	V
	VF2	IF = 40mA			1	
Maximum reverse current	IR	VR=30V			200	nA
Type junction capacitance	CT	VR = 0V, f = 1MHz			5	pF
Reverse Recovery time	Trr	IF=IR=10mA Irr=1mA, RL=100 Ω			5	ns

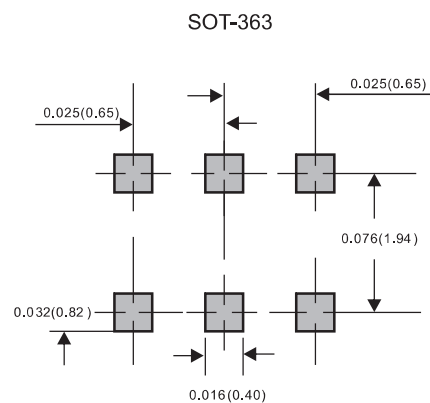
Rating and characteristic curves



Pinning information

Type number	Marking code	Simplified outline	Symbol
BAS40DW-04-Q1	K44 / 43D		
BAS40DW-05-Q1	K45 / 43C		
BAS40DW-06-Q1	K46 / 43B		

Suggested solder pad layout



Dimensions in inches and (millimeters)